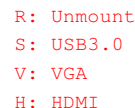



AMD RS880P+SB950 Block Diagram

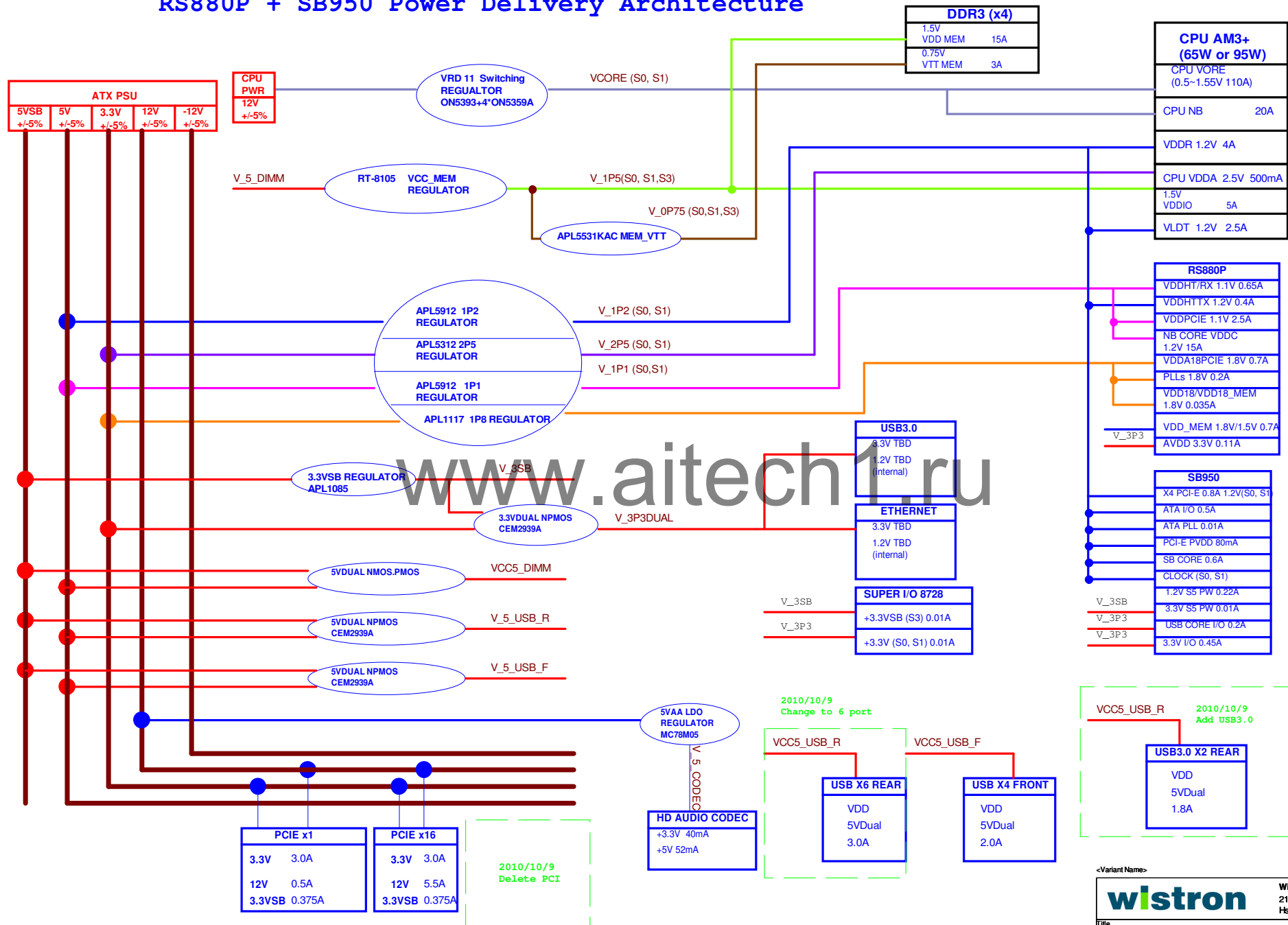
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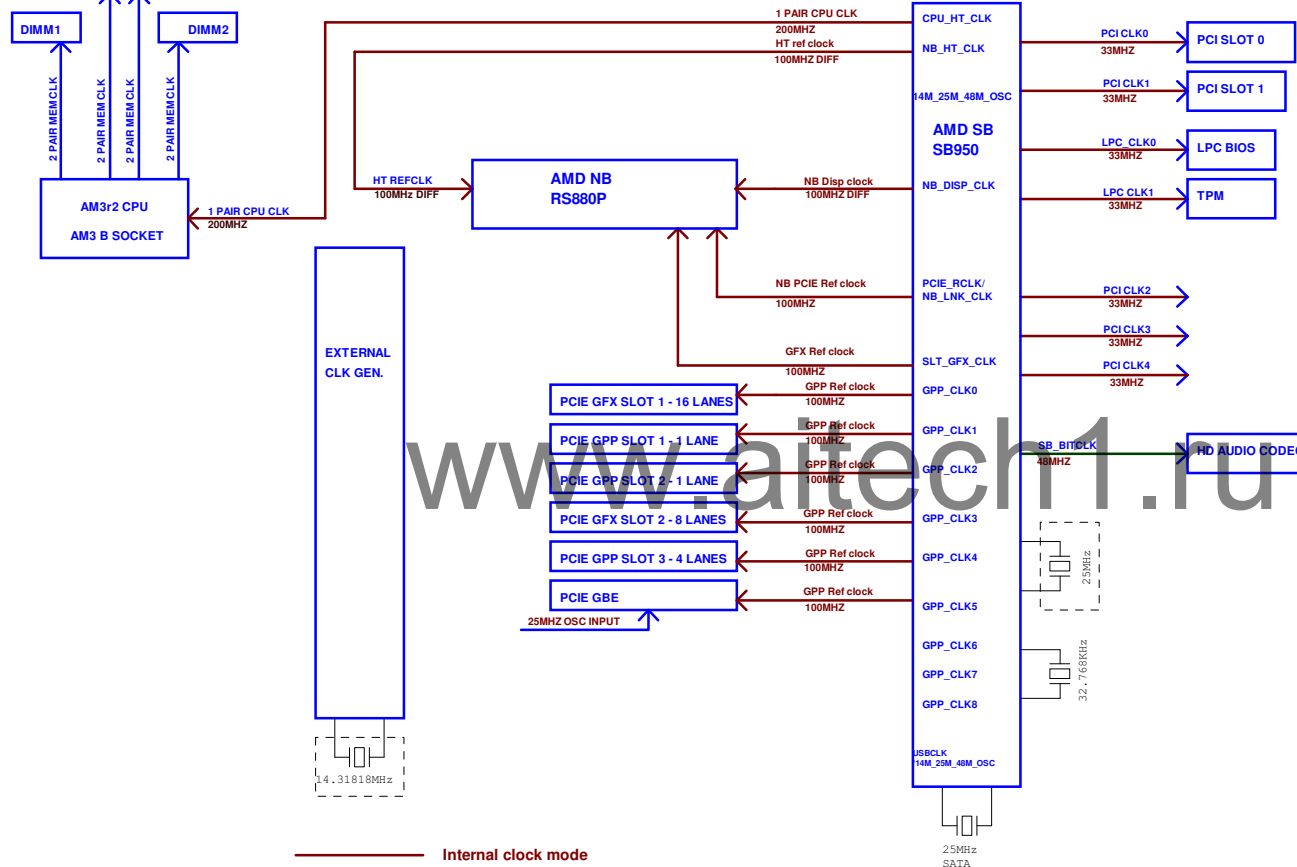
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RS880P + SB950 Power Delivery Architecture



2010/10/9

Change to Internal Clock mode



<Variant Name>

wistron

Wistron Incorporated
21F, 88, Hei Tai Wu Rd
Hsichih, Taipei

Title

CLOCK MAP

Size

Document Number

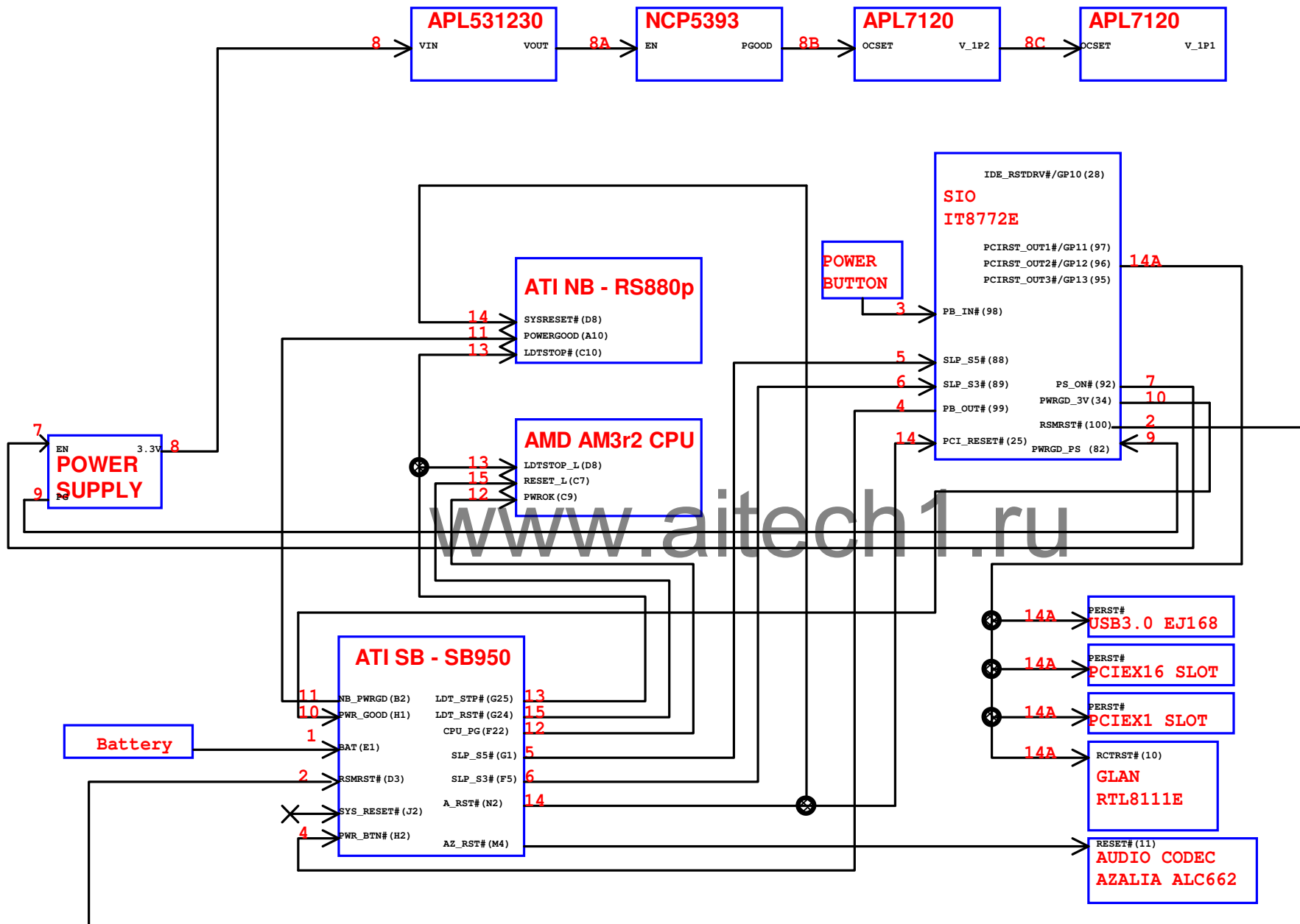
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Date: Thursday, March 31, 2011

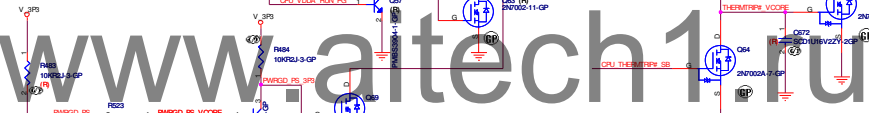
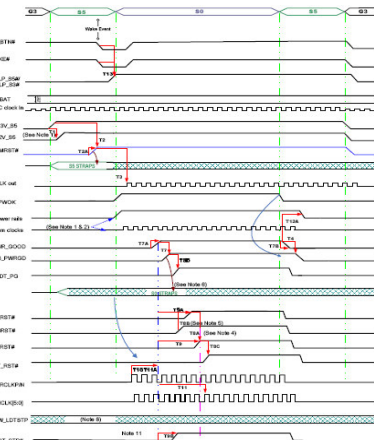
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RESET/POWER GOOD MAP

RSMRST

Del Discrete RSMRST circuit



PNB name	PNB ID	PNB GTYPE	POWER WELL	Default Func	Default State	USAGE (Bt cells)	Strap	50	52	54	55	SET	Notes
A005G000	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G001	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G002	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G003	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G004	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G005	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G006	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G007	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G008	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G009	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G010	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G011	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G012	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G013	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G014	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G015	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G016	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G017	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G018	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G019	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G020	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G021	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G022	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G023	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G024	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G025	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G026	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G027	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G028	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G029	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G030	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G031	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G032	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G033	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G034	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G035	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G036	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G037	A481	IO	3.3V I/O (V _{DD} IO)	PO	Output High	no use						GPIO High	
A012G038	A48												

PWR NAME	PWR	GPO TYPE	POWER VELL	Default Func	Default State	USAGE (NET name)	Strap	S0	S3	S4	S5	SET	Notes
AS11GPO001	A60	IO	3.3V I-S/V tolerance	PDI	Output High	no use						GPO High	
AS11GPO002	A60	IO	3.3V I-S/V tolerance	PDI	Input	Input 3.3V I-V						GPO High	
INTGPO001	A40	IO	3.3V I-S/V tolerance	PDI	Input 3.3V I-V	no use						GPO High	
AS11GPO003	A40	IO	3.3V I-S/V tolerance	PDI	Input 3.3V I-V	no use						GPO High	
INTGPO002	A44	IO	3.3V I-S/V tolerance	PDI	Input 3.3V I-V	no use						GPO High	
PDI4GPO001	U01	O	3.3V I-S/V tolerance	PDI4G	Output	PDI4-CLV3	Level					OBSS	10k to V3
PDI4GPO002	U01	O	3.3V I-S/V tolerance	PDI4G	Output	PDI4-CLV3	Level					OBSS	10k to GND
PDI4GPO003	U04	O	3.3V I-S/V tolerance	PDI4G	Output	PDI4-CLV3	Level					OBSS	10k to V3
PDI4GPO004	U04	O	3.3V I-S/V tolerance	PDI4G	Output	PDI4-CLV3	Level					OBSS	10k to V3
REQ1GPO001	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO002	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO003	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO004	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO005	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO006	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO007	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO008	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO009	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO010	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO011	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO012	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO013	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO014	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO015	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO016	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO017	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO018	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO019	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO020	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO021	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO022	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO023	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO024	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO025	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO026	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO027	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO028	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO029	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO030	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO031	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO032	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO033	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO034	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO035	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO036	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO037	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO038	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO039	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO040	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO041	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO042	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO043	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO044	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO045	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO046	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO047	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO048	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO049	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO050	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO051	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO052	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO053	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO054	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO055	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO056	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO057	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO058	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO059	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO060	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO061	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO062	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO063	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO064	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO065	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO066	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO067	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO068	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO069	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO070	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO071	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO072	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO073	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO074	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO075	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO076	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO077	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO078	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO079	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO080	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO081	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO082	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO083	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO084	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO085	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO086	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO087	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO088	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO089	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO090	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO091	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO092	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO093	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO094	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO095	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO096	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO097	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO098	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO099	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	
REQ1GPO100	AHS	O	3.3V I-S/V tolerance	PDI	Input 15K I-V	no use						High	

[illegible]

EC-1000-001	EC-1000-001	2001	EC-1000-001	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-002	EC-1000-002	2002	EC-1000-002	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-003	EC-1000-003	2003	EC-1000-003	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-004	EC-1000-004	2004	EC-1000-004	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-005	EC-1000-005	2005	EC-1000-005	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-006	EC-1000-006	2006	EC-1000-006	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-007	EC-1000-007	2007	EC-1000-007	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-008	EC-1000-008	2008	EC-1000-008	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-009	EC-1000-009	2009	EC-1000-009	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-010	EC-1000-010	2010	EC-1000-010	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-011	EC-1000-011	2011	EC-1000-011	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-012	EC-1000-012	2012	EC-1000-012	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-013	EC-1000-013	2013	EC-1000-013	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-014	EC-1000-014	2014	EC-1000-014	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-015	EC-1000-015	2015	EC-1000-015	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-016	EC-1000-016	2016	EC-1000-016	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-017	EC-1000-017	2017	EC-1000-017	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-018	EC-1000-018	2018	EC-1000-018	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-019	EC-1000-019	2019	EC-1000-019	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-020	EC-1000-020	2020	EC-1000-020	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-021	EC-1000-021	2021	EC-1000-021	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-022	EC-1000-022	2022	EC-1000-022	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-023	EC-1000-023	2023	EC-1000-023	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-024	EC-1000-024	2024	EC-1000-024	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-025	EC-1000-025	2025	EC-1000-025	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-026	EC-1000-026	2026	EC-1000-026	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-027	EC-1000-027	2027	EC-1000-027	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-028	EC-1000-028	2028	EC-1000-028	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-029	EC-1000-029	2029	EC-1000-029	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-030	EC-1000-030	2030	EC-1000-030	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-031	EC-1000-031	2031	EC-1000-031	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-032	EC-1000-032	2032	EC-1000-032	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-033	EC-1000-033	2033	EC-1000-033	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-034	EC-1000-034	2034	EC-1000-034	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-035	EC-1000-035	2035	EC-1000-035	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-036	EC-1000-036	2036	EC-1000-036	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-037	EC-1000-037	2037	EC-1000-037	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-038	EC-1000-038	2038	EC-1000-038	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-039	EC-1000-039	2039	EC-1000-039	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-040	EC-1000-040	2040	EC-1000-040	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-041	EC-1000-041	2041	EC-1000-041	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-042	EC-1000-042	2042	EC-1000-042	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-043	EC-1000-043	2043	EC-1000-043	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-044	EC-1000-044	2044	EC-1000-044	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-045	EC-1000-045	2045	EC-1000-045	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-046	EC-1000-046	2046	EC-1000-046	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-047	EC-1000-047	2047	EC-1000-047	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-048	EC-1000-048	2048	EC-1000-048	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-049	EC-1000-049	2049	EC-1000-049	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-050	EC-1000-050	2050	EC-1000-050	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-051	EC-1000-051	2051	EC-1000-051	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-052	EC-1000-052	2052	EC-1000-052	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-053	EC-1000-053	2053	EC-1000-053	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-054	EC-1000-054	2054	EC-1000-054	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-055	EC-1000-055	2055	EC-1000-055	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-056	EC-1000-056	2056	EC-1000-056	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-057	EC-1000-057	2057	EC-1000-057	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-058	EC-1000-058	2058	EC-1000-058	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-059	EC-1000-059	2059	EC-1000-059	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-060	EC-1000-060	2060	EC-1000-060	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-061	EC-1000-061	2061	EC-1000-061	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-062	EC-1000-062	2062	EC-1000-062	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-063	EC-1000-063	2063	EC-1000-063	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-064	EC-1000-064	2064	EC-1000-064	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-065	EC-1000-065	2065	EC-1000-065	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-066	EC-1000-066	2066	EC-1000-066	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-067	EC-1000-067	2067	EC-1000-067	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-068	EC-1000-068	2068	EC-1000-068	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-069	EC-1000-069	2069	EC-1000-069	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-070	EC-1000-070	2070	EC-1000-070	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-071	EC-1000-071	2071	EC-1000-071	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-072	EC-1000-072	2072	EC-1000-072	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-073	EC-1000-073	2073	EC-1000-073	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-074	EC-1000-074	2074	EC-1000-074	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-075	EC-1000-075	2075	EC-1000-075	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-076	EC-1000-076	2076	EC-1000-076	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-077	EC-1000-077	2077	EC-1000-077	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-078	EC-1000-078	2078	EC-1000-078	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-079	EC-1000-079	2079	EC-1000-079	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-080	EC-1000-080	2080	EC-1000-080	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-081	EC-1000-081	2081	EC-1000-081	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-082	EC-1000-082	2082	EC-1000-082	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-083	EC-1000-083	2083	EC-1000-083	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-084	EC-1000-084	2084	EC-1000-084	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-085	EC-1000-085	2085	EC-1000-085	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-086	EC-1000-086	2086	EC-1000-086	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-087	EC-1000-087	2087	EC-1000-087	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-088	EC-1000-088	2088	EC-1000-088	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-089	EC-1000-089	2089	EC-1000-089	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-090	EC-1000-090	2090	EC-1000-090	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-091	EC-1000-091	2091	EC-1000-091	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-092	EC-1000-092	2092	EC-1000-092	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-093	EC-1000-093	2093	EC-1000-093	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-094	EC-1000-094	2094	EC-1000-094	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-095	EC-1000-095	2095	EC-1000-095	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-096	EC-1000-096	2096	EC-1000-096	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-097	EC-1000-097	2097	EC-1000-097	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-098	EC-1000-098	2098	EC-1000-098	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-099	EC-1000-099	2099	EC-1000-099	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-100	EC-1000-100	2100	EC-1000-100	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-101	EC-1000-101	2101	EC-1000-101	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-102	EC-1000-102	2102	EC-1000-102	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-103	EC-1000-103	2103	EC-1000-103	1.0	EC-1000	Input: 10K PU	Output: 10K PU	10W	EC-1000
EC-1000-104	EC-1								

Notes: Z = Tri-state, I/O = Bidirectional, I = Input, O = Output, OD = Open Drain, OC = Open Collector, U = Unpowered, L = Low, H=High

GPIO#	Pin Location	MUX Function	USAGE (NET name)	Function	Power By	Note
GPIO3	7	CPU_PGGP29	I/O SW_N	Only PH		
GP29	5	ATPFG_GP29	ATPWRMGW_SIO_D	PH	AVCC3	10K to V_SS
GP31	63	PIVSR1GP31	SYS_FAN1_PWM	GPO High	AVCC3	10K to V_SS
GP32	63	R1R1GP32	R1R1	GPO High	AVCC3	8.2K to V_3P3
GP33	62	PWVRD1GP33	SYS_FAN1_PWM	GPO High	AVCC3	8.2K to V_SS
GP34	62	FAH1GP34	SYS_FAN1_PWM	SYS_FAN1_PWM	AVCC3	8.2K to V_SS
GP37	3	FAN1_TACH_GP37	SYS_FAN1_TACH	SYS_FAN1_TACH	AVCC3	
GP11	41	PKCRST1GP11	PK_CRST1	GPO High	AVCC3	10K to V_SS
GP10	41	PKCRST1GP10	IR_CR_C	GPO High	AVCC3	10K to V_SS
GP12	10	PKCRST1GP12	THERMAL_SHUTD	GPO High	AVCC3	10K to V_3P3
GP22	3	PIVSR1GP22	SYS_FAN1_PWM	GPO High	AVCC3	10K to V_SS
GP40	36	SVBSVRW1GP40	SIO_PWVRD1	DO	VSSB	10K to V_SS
GP41	36	SVBSVRW1GP41	SIO_PWVRD1	GPO High	AVCC3	10K to V_SS
GP43	32	SVBSVRW1GP43	SIO_PWVRD1	GPO High	AVCC3	10K to V_SS
GP44	32	SVBSVRW1GP44	SIO_PWVRD1	GPO High	AVCC3	10K to V_SS
GP45	30	PWVRD1GP45	PWVRD1_OUT	GPO High	AVCC3	10K to V_SS
GP46	28	PKCRST1GP46	SIO_CRST1	GPO High	AVCC3	10K to V_SS
GP81	2	FAN1_TACH_GP81	CPU_FAN1_PWM	CPU_FAN1_PWM Output/High	AVCC3	
GP82	1	FAH1GP82	CPU_FAN1_TACH	CPU_FAN1_TACH Output/High	AVCC3	
GP83	34	SUSCRGP83	SUSP_R*	SUSP*	VSSB	
GP84	34	PKCRST1GP84	PK_CRST1	DO	VSSB	
GP85	42	RSRSTR1GP85	RSRSTR1	DO	VSSB	10K to V_SS
GP49	40	MCLK_GP49	SIO_MCLK	DO	VSSB	8.2K to V_5_USB_2
GP50	40	MCLK_GP50	SIO_MCLK	DO	VSSB	8.2K to V_5_USB_2
GP51	40	MCLK_GP51	SIO_MCLK	DO	VSSB	8.2K to V_5_USB_2
GP56	38	KOLK_GP56	SIO_KOLK	DO	VSSB	8.2K to V_5_USB_2
GP57	37	KOLK_GP57	SIO_KOLK	DO	VSSB	8.2K to V_5_USB_2
GP58	20	RSRSTR1GP58	RSRSTR1	DO	VSSB	8.2K to V_5_USB_2
GP65	55	VLD1_GP65	BOARD1	GPIO LOW	AVCC3	8.2K to V_3P3
GP66	55	VLD1_GP66	BOARD1	GPIO LOW	AVCC3	PULL UP to GND

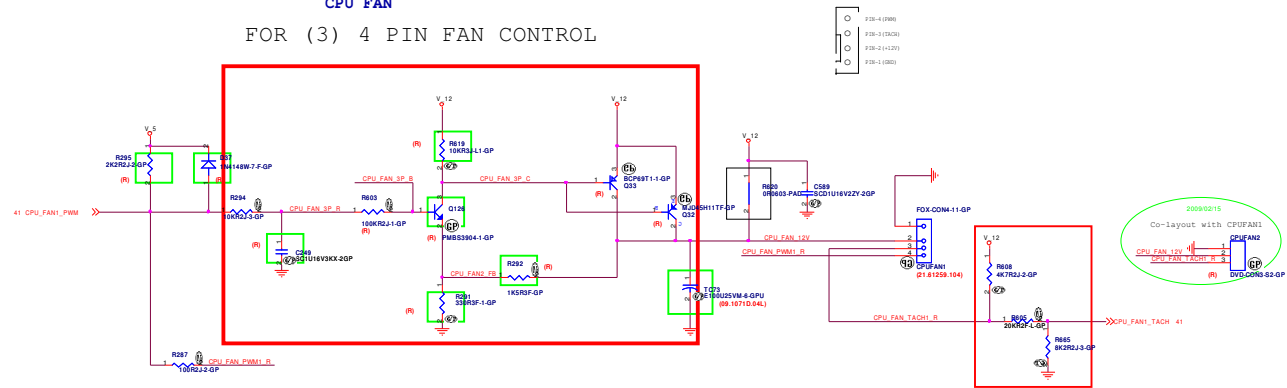
CPU FAN

41 CPU_FAN1_PWM >>>
41 CPU_FAN1_TACH <<<

SYS FAN


41 SYS_FAN2_PWM >>>
41 SYS_FAN2_TACH <<<

CPU FAN FOR (3) 4 PIN FAN CONTROL



2010/10/7
Delete External Clock Generator

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<Variant Name>		
		Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei
Title EXTERNAL CLOCK GENERATOR		
Size Custom	Document Number NADIA	Rev SA
Date: Thursday, March 31, 2011 1 Sheet 8 of 48		

HT Interface

15 HT_CLKIN1_P << HT_CLKIN1_P
15 HT_CLKIN1_N << HT_CLKIN1_N
15 HT_CLKIN0_P << HT_CLKIN0_P
15 HT_CLKIN0_N << HT_CLKIN0_N

15 HT_CTLIN1_P << HT_CTLIN1_P
15 HT_CTLIN1_N << HT_CTLIN1_N
15 HT_CTLIN0_P << HT_CTLIN0_P
15 HT_CTLIN0_N << HT_CTLIN0_N

15 HT_CLKOUT1_P << HT_CLKOUT1_P
15 HT_CLKOUT1_N << HT_CLKOUT1_N
15 HT_CLKOUT0_P << HT_CLKOUT0_P
15 HT_CLKOUT0_N << HT_CLKOUT0_N

15 HT_CTLOUT1_P << HT_CTLOUT1_P
15 HT_CTLOUT1_N << HT_CTLOUT1_N
15 HT_CTLOUT0_P << HT_CTLOUT0_P
15 HT_CTLOUT0_N << HT_CTLOUT0_N

15 HT_CADIN7_P << HT_CADIN7_P
15 HT_CADIN7_N << HT_CADIN7_N
15 HT_CADIN6_P << HT_CADIN6_P
15 HT_CADIN6_N << HT_CADIN6_N
15 HT_CADIN5_P << HT_CADIN5_P
15 HT_CADIN5_N << HT_CADIN5_N
15 HT_CADIN4_P << HT_CADIN4_P
15 HT_CADIN4_N << HT_CADIN4_N
15 HT_CADIN3_P << HT_CADIN3_P
15 HT_CADIN3_N << HT_CADIN3_N
15 HT_CADIN2_P << HT_CADIN2_P
15 HT_CADIN2_N << HT_CADIN2_N
15 HT_CADIN1_P << HT_CADIN1_P
15 HT_CADIN1_N << HT_CADIN1_N
15 HT_CADIN0_P << HT_CADIN0_P
15 HT_CADIN0_N << HT_CADIN0_N

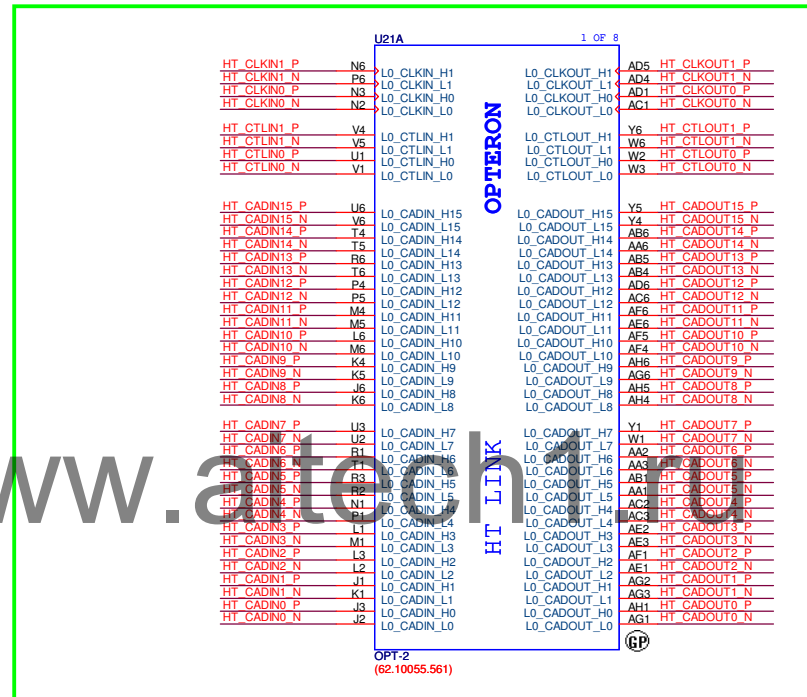
15 HT_CADIN15_P << HT_CADIN15_P
15 HT_CADIN15_N << HT_CADIN15_N
15 HT_CADIN14_P << HT_CADIN14_P
15 HT_CADIN14_N << HT_CADIN14_N
15 HT_CADIN13_P << HT_CADIN13_P
15 HT_CADIN13_N << HT_CADIN13_N
15 HT_CADIN12_P << HT_CADIN12_P
15 HT_CADIN12_N << HT_CADIN12_N
15 HT_CADIN11_P << HT_CADIN11_P
15 HT_CADIN11_N << HT_CADIN11_N
15 HT_CADIN10_P << HT_CADIN10_P
15 HT_CADIN10_N << HT_CADIN10_N
15 HT_CADIN9_P << HT_CADIN9_P
15 HT_CADIN9_N << HT_CADIN9_N
15 HT_CADIN8_P << HT_CADIN8_P
15 HT_CADIN8_N << HT_CADIN8_N

15 HT_CADOUT7_P << HT_CADOUT7_P
15 HT_CADOUT7_N << HT_CADOUT7_N
15 HT_CADOUT6_P << HT_CADOUT6_P
15 HT_CADOUT6_N << HT_CADOUT6_N
15 HT_CADOUT5_P << HT_CADOUT5_P
15 HT_CADOUT5_N << HT_CADOUT5_N
15 HT_CADOUT4_P << HT_CADOUT4_P
15 HT_CADOUT4_N << HT_CADOUT4_N
15 HT_CADOUT3_P << HT_CADOUT3_P
15 HT_CADOUT3_N << HT_CADOUT3_N
15 HT_CADOUT2_P << HT_CADOUT2_P
15 HT_CADOUT2_N << HT_CADOUT2_N
15 HT_CADOUT1_P << HT_CADOUT1_P
15 HT_CADOUT1_N << HT_CADOUT1_N
15 HT_CADOUT0_P << HT_CADOUT0_P
15 HT_CADOUT0_N << HT_CADOUT0_N

15 HT_CADOUT15_P << HT_CADOUT15_P
15 HT_CADOUT15_N << HT_CADOUT15_N
15 HT_CADOUT14_P << HT_CADOUT14_P
15 HT_CADOUT14_N << HT_CADOUT14_N
15 HT_CADOUT13_P << HT_CADOUT13_P
15 HT_CADOUT13_N << HT_CADOUT13_N
15 HT_CADOUT12_P << HT_CADOUT12_P
15 HT_CADOUT12_N << HT_CADOUT12_N
15 HT_CADOUT11_P << HT_CADOUT11_P
15 HT_CADOUT11_N << HT_CADOUT11_N
15 HT_CADOUT10_P << HT_CADOUT10_P
15 HT_CADOUT10_N << HT_CADOUT10_N
15 HT_CADOUT9_P << HT_CADOUT9_P
15 HT_CADOUT9_N << HT_CADOUT9_N
15 HT_CADOUT8_P << HT_CADOUT8_P
15 HT_CADOUT8_N << HT_CADOUT8_N

2010/11/11

Change CPU socket to 62.10055.561



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<Variant Name>

wistron		Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
Title CPU HT Interface			
Size A3	Document Number NADIA		Rev SA
Date:	Thursday, March 31, 2011	Sheet	9 of 48

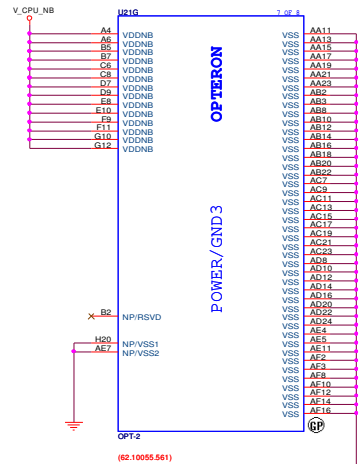
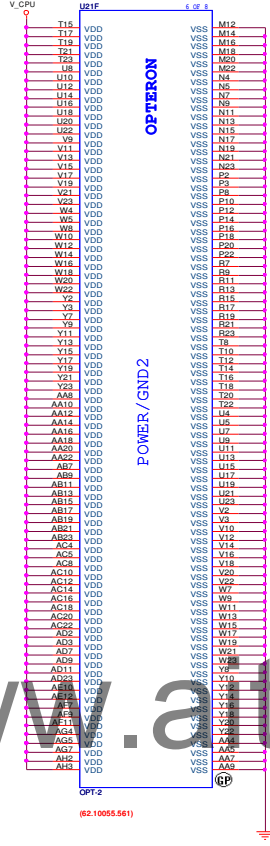
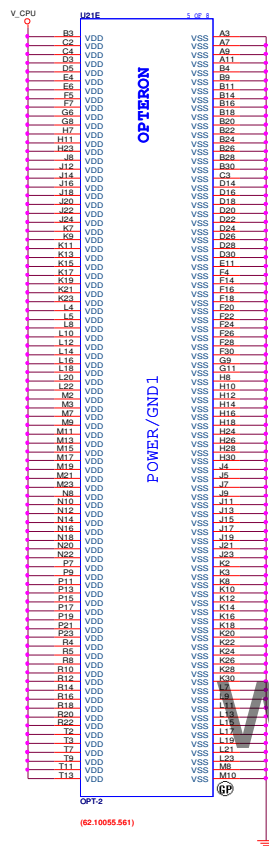
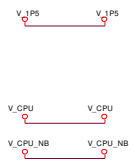


Change CPU socket to 62.10055.561

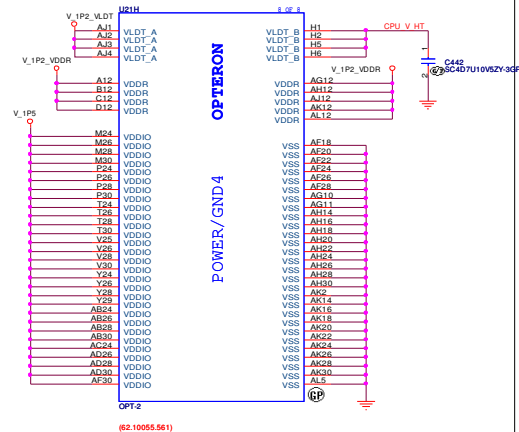
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POWER



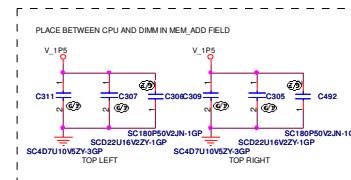
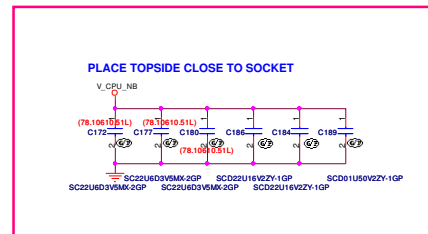
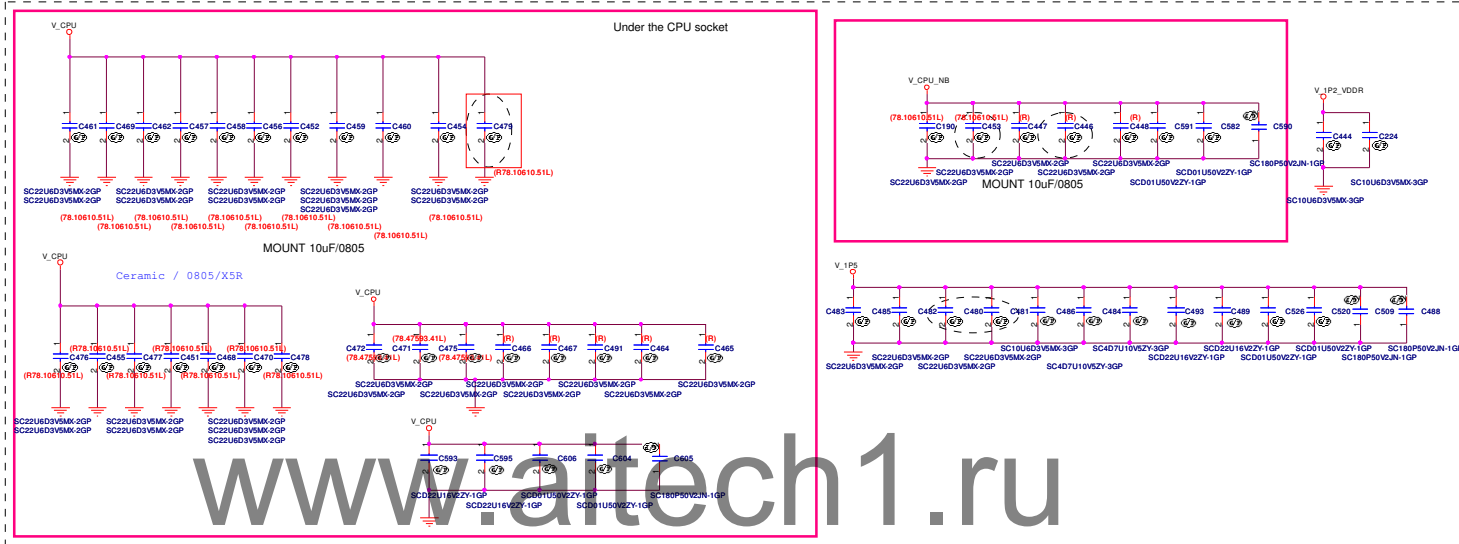
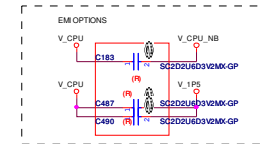
Either VLDT_A or VLDT_B is connected to V_IP2

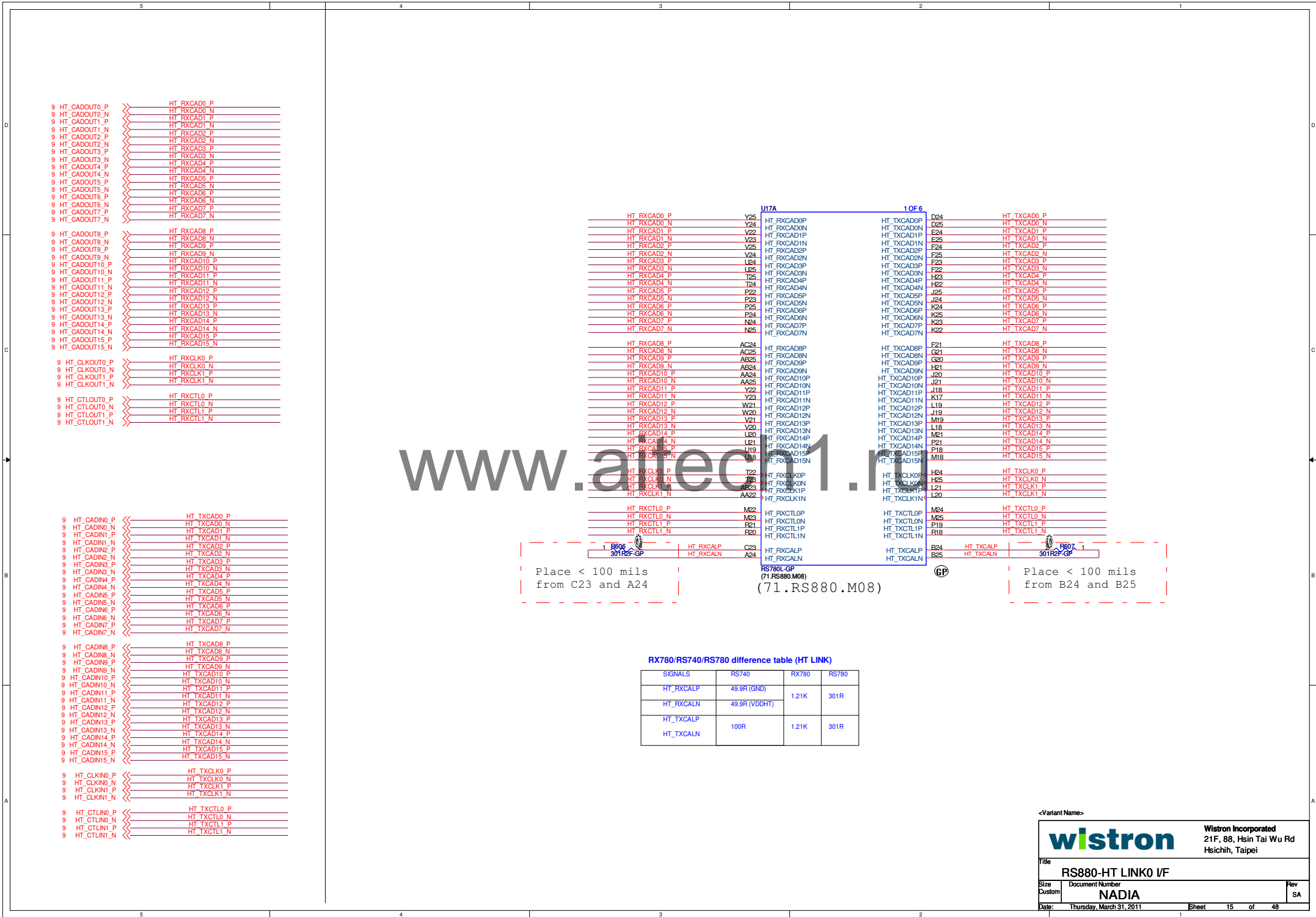


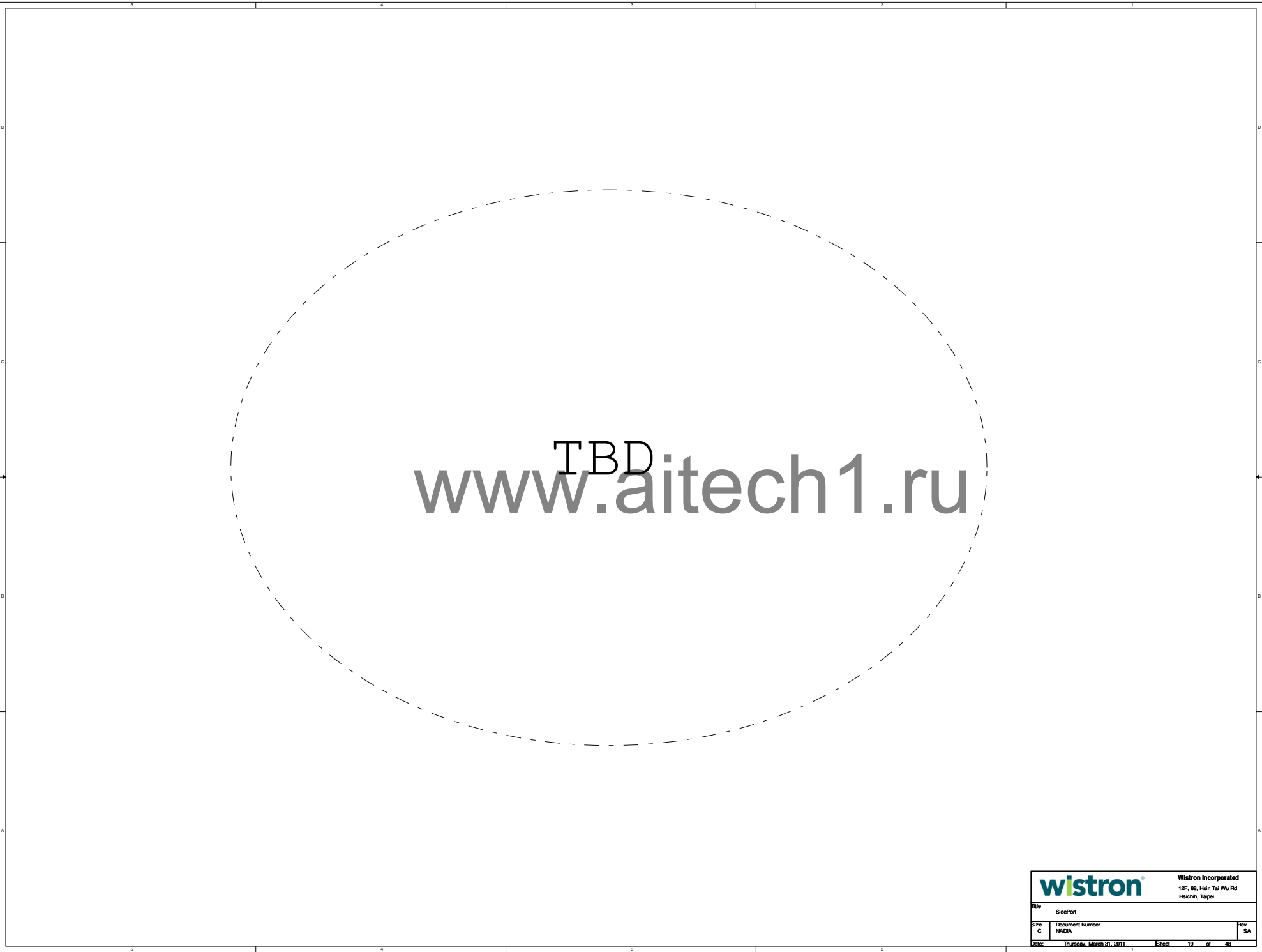
2010/11/11
Change CPU socket to 62.10055.561

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V_CPU_NB V_CPU_NB

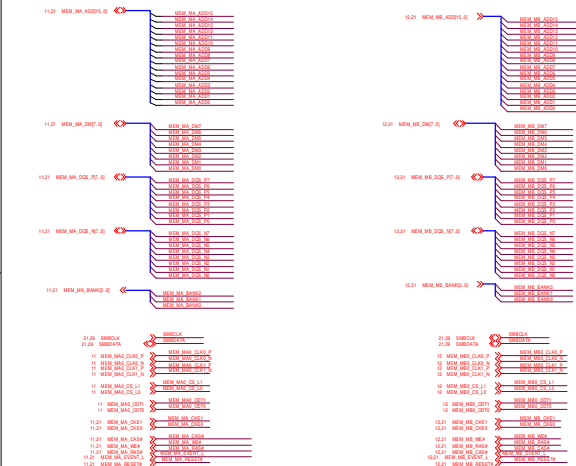






Wistron Incorporated
12F, 88, Hsin Tai Wu Rd
Hsinchu, Taipei

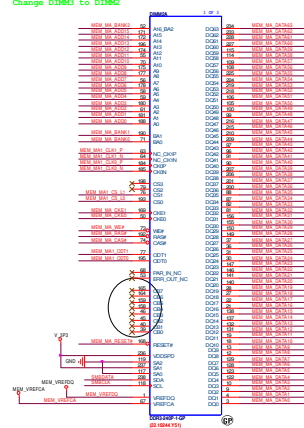
Title		SidePort	
Size	C	Document Number	NA00A
Date: Thursday, March 31, 2011		Sheet	19 of 48



Lotes 22.10244.K41(Blue) and
22.10244.K31(Black)
Type 22.10244.K21(Blue) and
22.10244.K51(Black)

2010/09/29
Change DIMM3 to DIM2

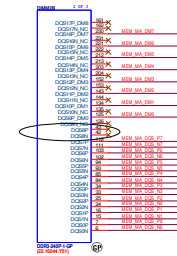
DIMM 2



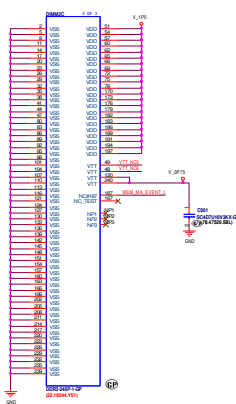
Pin	Signal
1	VDD
2	VSS
3	VDD
4	VSS
5	VDD
6	VSS
7	VDD
8	VSS
9	VDD
10	VSS
11	VDD
12	VSS
13	VDD
14	VSS
15	VDD
16	VSS
17	VDD
18	VSS
19	VDD
20	VSS
21	VDD
22	VSS
23	VDD
24	VSS
25	VDD
26	VSS
27	VDD
28	VSS
29	VDD
30	VSS
31	VDD
32	VSS

2010/10/10
Change DIMM2 address to 010

2010/09/29
Change DIMM3 to DIM2



2010/09/29
Change DIMM3 to DIM2



2010/09/29
Change DIMM4 to DIMM1

DIMM 1



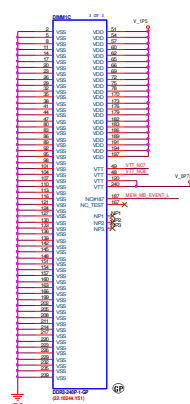
Pin	Signal
1	VDD
2	VSS
3	VDD
4	VSS
5	VDD
6	VSS
7	VDD
8	VSS
9	VDD
10	VSS
11	VDD
12	VSS
13	VDD
14	VSS
15	VDD
16	VSS
17	VDD
18	VSS
19	VDD
20	VSS
21	VDD
22	VSS
23	VDD
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25	VDD
26	VSS
27	VDD
28	VSS
29	VDD
30	VSS
31	VDD
32	VSS

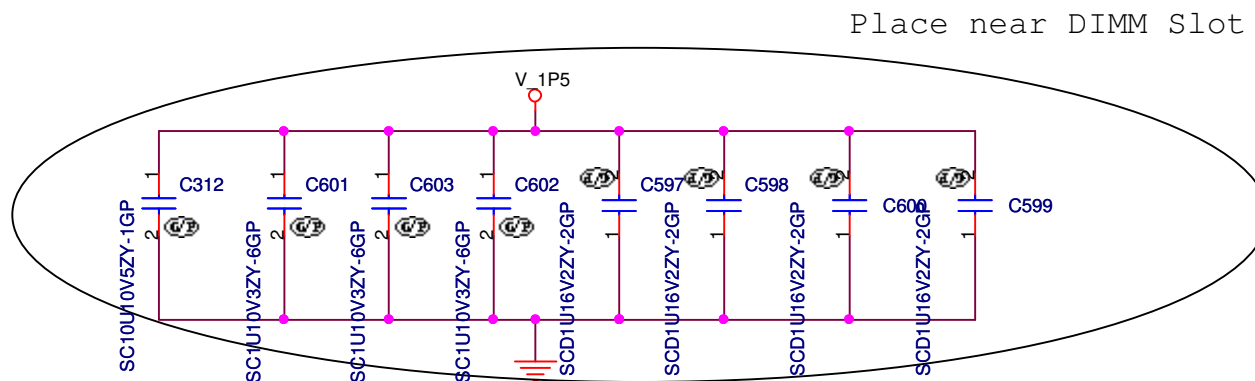
2010/10/10
Change DIMM1 address to 011

2010/09/29
Change DIMM4 to DIMM1



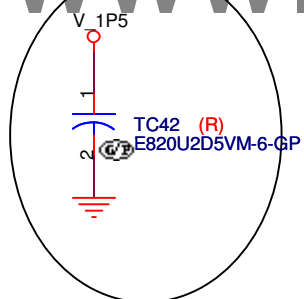
2010/09/29
Change DIMM4 to DIMM1





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Place between DIMM4 Slot and CPU



<Variant Name>

wistron

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Hsichih, Taipei

Title

MEM TERMS & DECAPS

Size
A

Document Number

NADIA

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(H) : HDMI

DDC

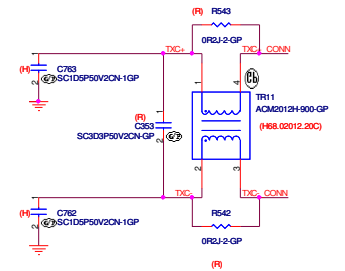
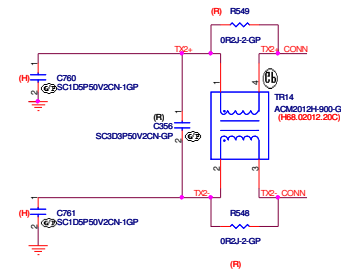
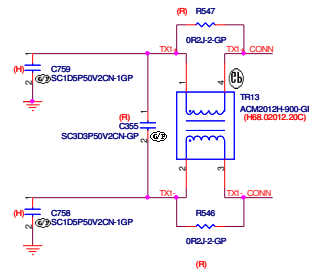
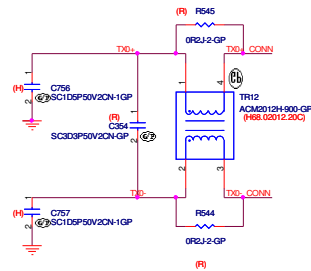
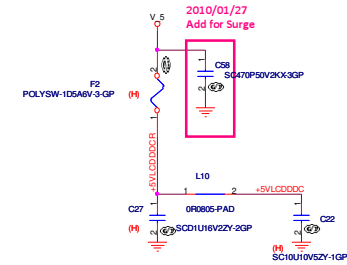
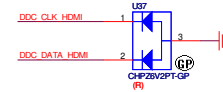
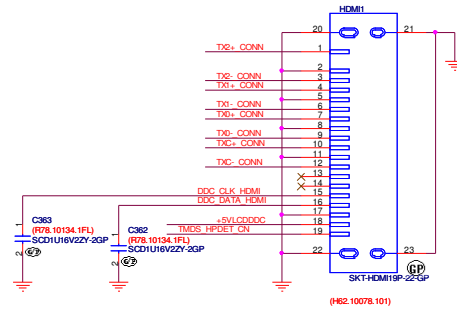
- 17 DDC_CLK_HDMI
- 17 DDC_DATA_HDMI

CLK DATA

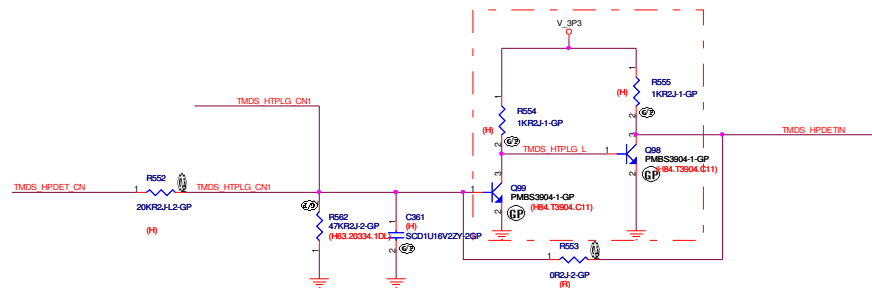
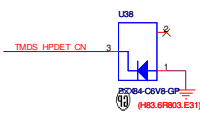
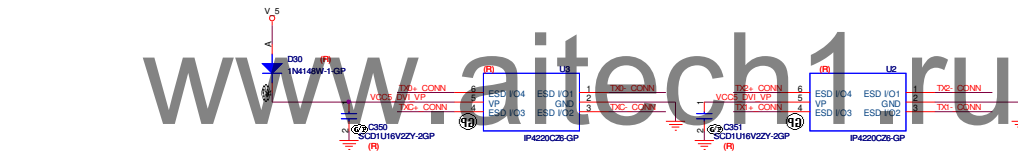
- 17 TMDS_CLKN
- 17 TMDS_CLKP
- 17 TMDS_D0N
- 17 TMDS_D0P
- 17 TMDS_D1N
- 17 TMDS_D1P
- 17 TMDS_D2N
- 17 TMDS_D2P

TMDS HPD

- 17 TMDS_HPDETIN



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CLOSED TO HDMI CONNECTOR

<Variant Name>			
wistron			
Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsinchu, Taipei			
HDMI CONN			
File	Document Number	Rev	SA
Size	NADIA		
Custom			
Date:	Thursday, March 31, 2011	Sheet	21 of 48

Video Filter

(V) : VGA

Place PI filter close to VGA connector
5 mil trace width/20 mil spacing

RGB

17 VGA_RED >>
17 VGA_GREEN >>
17 VGA_BLUE >>

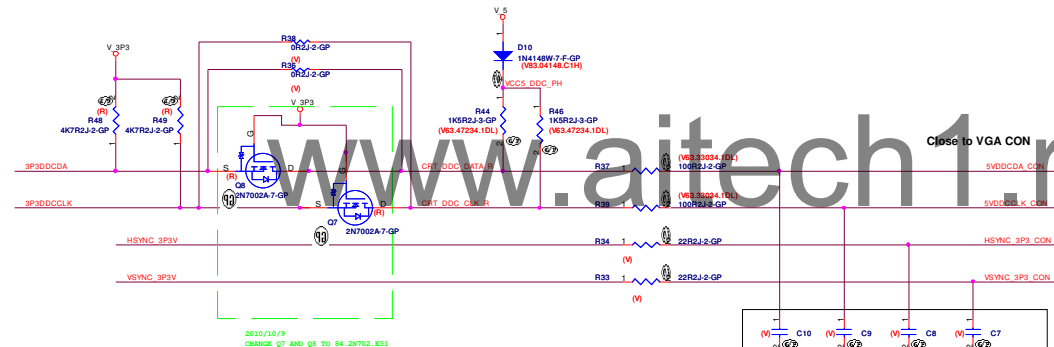
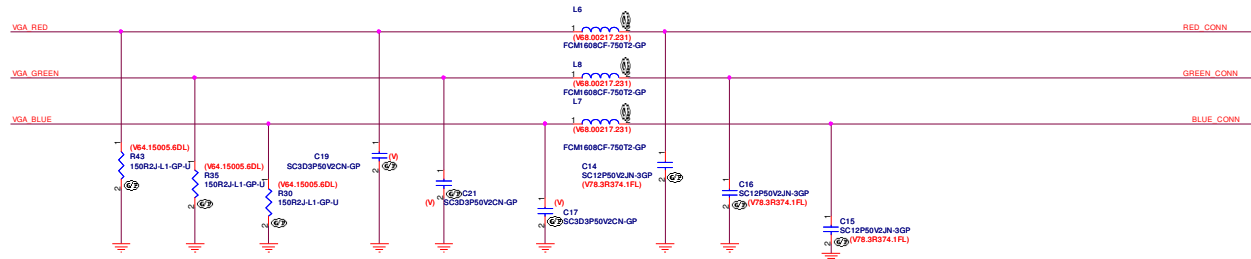
H&V_SYNC

17 HSYNC_3P3V >>
17 VSYNC_3P3V >>

DDC

17 CRT_DDC_CLK >>
17 CRT_DDC_DATA >>

2009/8/9
Swap the wrong connections of VGA_DDC/DATA

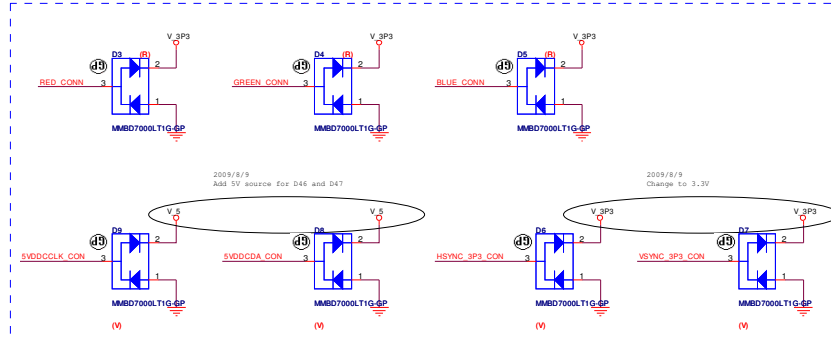


Close to VGA CON

34.00015.231 x 2
Screw for VGA connector
(No need for 20.20825.015)

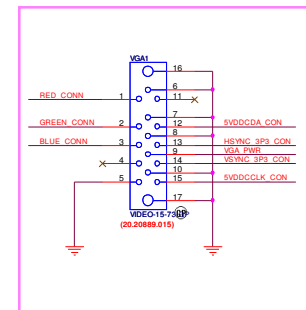
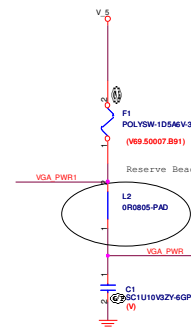
2011/02/08
Change VGA1 to 20.20889.015

2009/07/02



2009/8/9
Add 5V source for D46 and D47

2009/8/9
Change to 3.3V



<Variant Name>

wlstron
Wistron Incorporated
21F, 88, Hei Tai Wu Rd
Hsinchu, Taipei

VGA CONN		
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SB GFX S0 CLKP	SB GFX S0 CLKP
SB GFX S0 CLKN	SB GFX S0 CLKN

TX signals

```

16 PEG_TXP[15..0] << EXP_16X_TXP[15..0]
16 PEG_TXN[15..0] << EXP_16X_TXN[15..0]

```

16 PEG_RXP[15..0] << EXP_16X_RXP[15..0]
16 PEG_RXN[15..0] << EXP_16X_RXN[15..0]

26,29 ALERT_CLK

26,29 ALERT_DATA

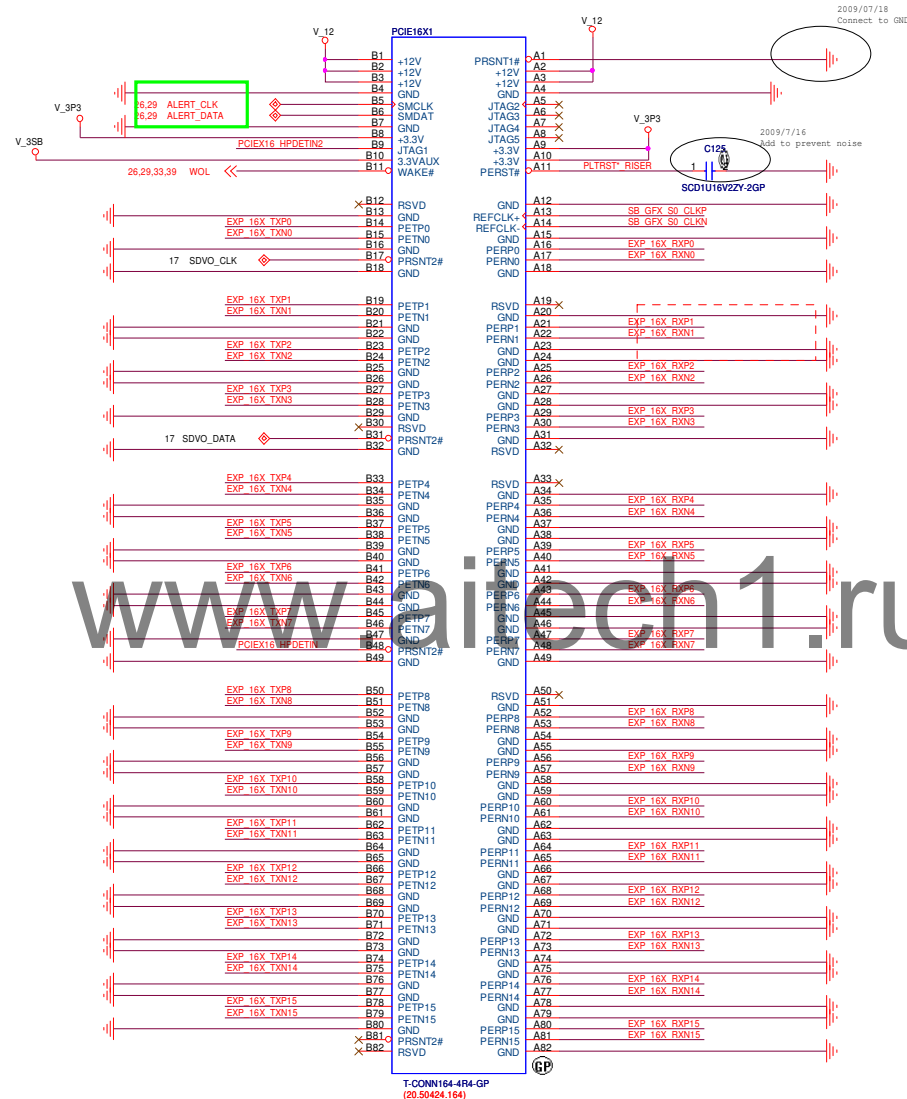
26,29,33,39 WOL <<—

17 SDVO_CLK <<—

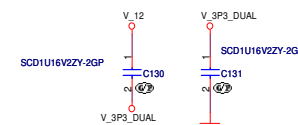
17 SDVO_DATA <<—

26,28 PLTRST* RISER >>—

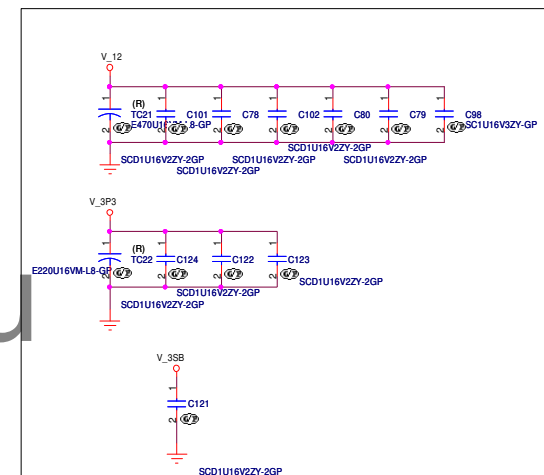
```
17 PCIE16_HPDETIN    << PCIE16_HPDETIN
17 PCIE16_HPDETIN2   << PCIE16_HPDETIN2
```



Add 跨Moat電容



PLACE CAPS NEAR PCI-Ex16 CONNECTORS



<Variant Name>



Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Tato	PCI-E X16 CONN
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Size	Document Number
Custom	NADIA

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SMBUS

25,29 ALERT_CLK >>>
25,29 ALERT_DATA >>>

WOL

25,29,33,39 WOL <<<

PCIE X1 RX FROM RS880

16 PCIE_X1_2_RXP >>> PCIE_X1_2_RXP
16 PCIE_X1_2_RXN >>> PCIE_X1_2_RXN

PCIE X1 TX FROM RS880

16 PCIE_X1_2_TXP >>> PCIE_X1_2_TXP
16 PCIE_X1_2_TXN >>> PCIE_X1_2_TXN

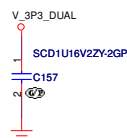
RST*

25,28 PLTRST*_RISER >>>

CLK

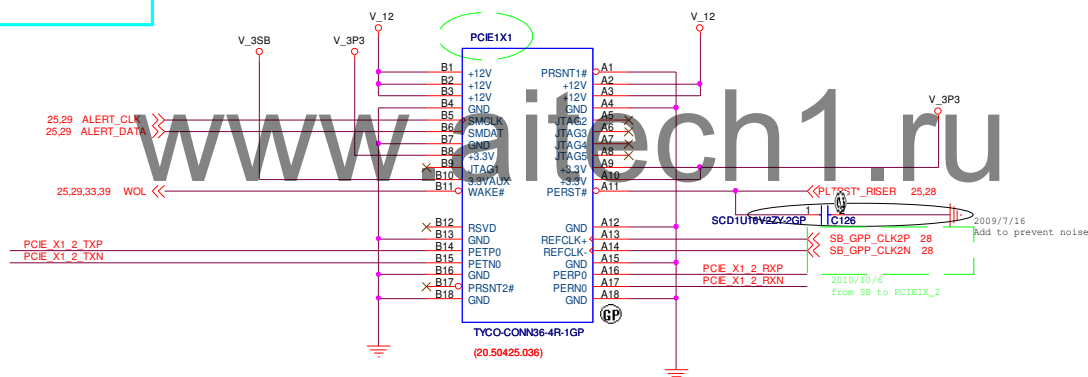
28 SB_GPP_CLK2P >>> SB_GPP_CLK2P
28 SB_GPP_CLK2N >>> SB_GPP_CLK2N

2010/11/29
Add 跨Moat電容

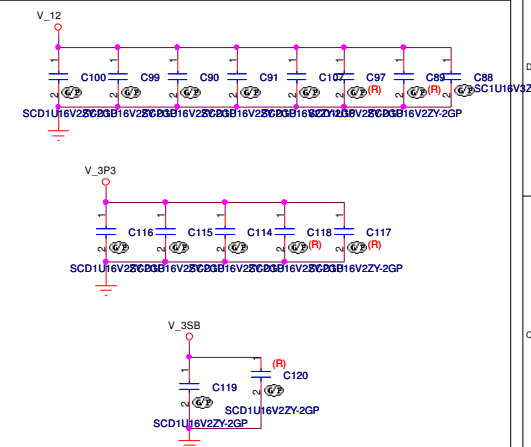


2010/9/30
Delete PCIE1X_1

2010/10/13
Rename it to PCIE1x1



PLACE CAPS NEAR PCI-Ex1 CONNECTORS



<Variant Name>	
wistron	
Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
Title PCI-E X1 CONN	
Size Custom	Document Number NADIA
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2010/09/29

Delete PCI1 SLOT

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<Variant Name>

wlstron

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title
PCI CONN

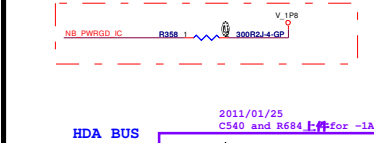
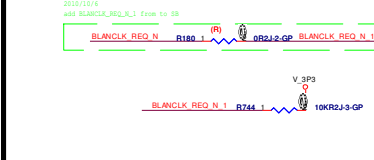
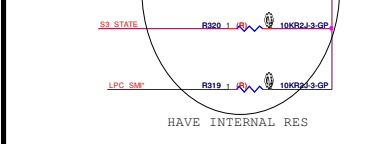
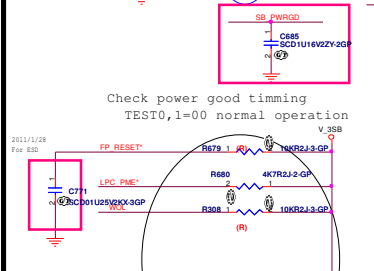
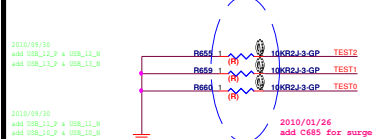
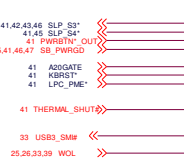
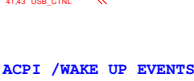
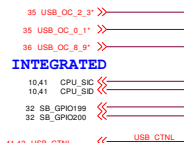
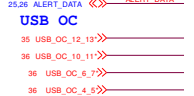
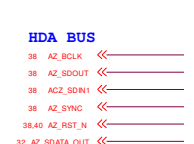
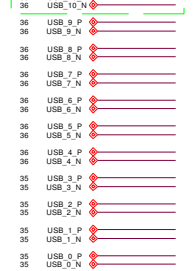
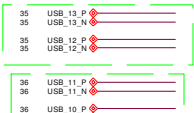
Size Custom Document Number
NADIA

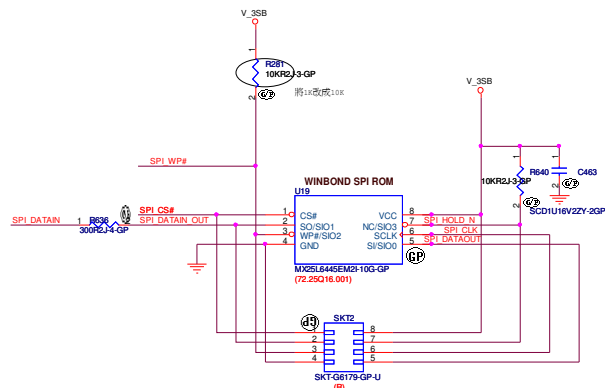
Rev
SA

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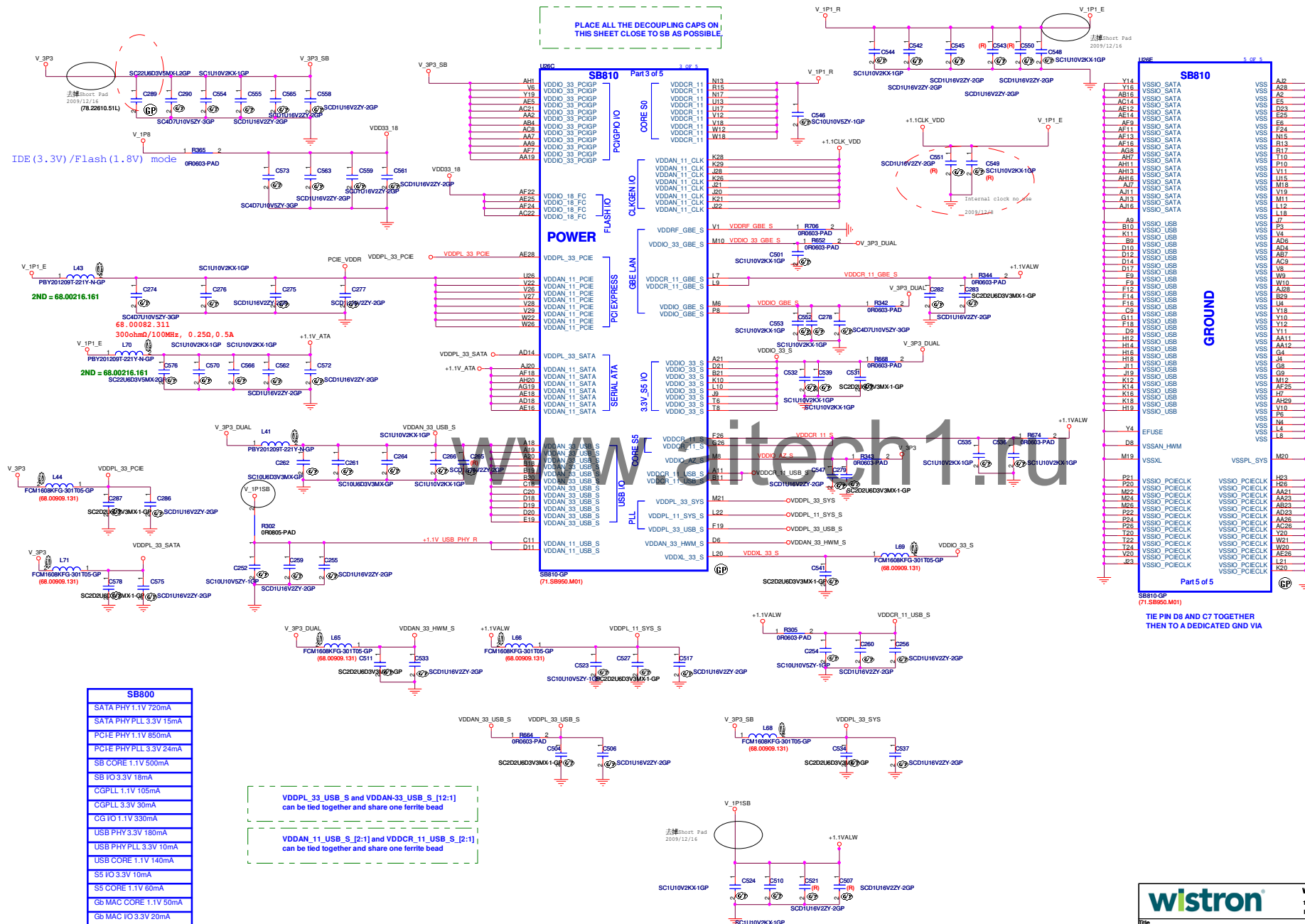
USB 2.0







PLACE ALL THE DECOUPLING CAPS ON
THIS SHEET CLOSE TO SB AS POSSIBLE



S8800
SATA PHY 1.1V 720mA
SATA PHY PLL 3.3V 15mA
PCI-E PHY 1.1V 850mA
SB PHY PLL 3.3V 24mA
SB CORE 1.1V 500mA
SB IO 3.3V 18mA
CGPLL 1.1V 105mA
CGPLL 3.3V 30mA
CG IO 1.1V 330mA
USB PHY 3.3V 180mA
USB PHY PLL 3.3V 10mA
USB CORE 1.1V 140mA
SS IO 3.3V 10mA
SS CORE 1.1V 60mA
Gb MAC CORE 1.1V 50mA
Gb MAC IO 3.3V 20mA

Wistron Incorporated
12F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

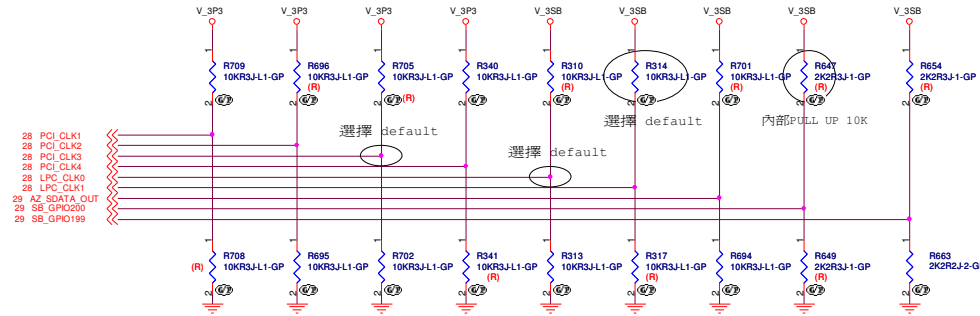
Title	POWER & DECOUPLING
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Size C	Document Number NADIA
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C	NADA
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NOTE: SB950 HAS INTERNAL 15K PULL UP RESISTOR FOR RTC_CLK



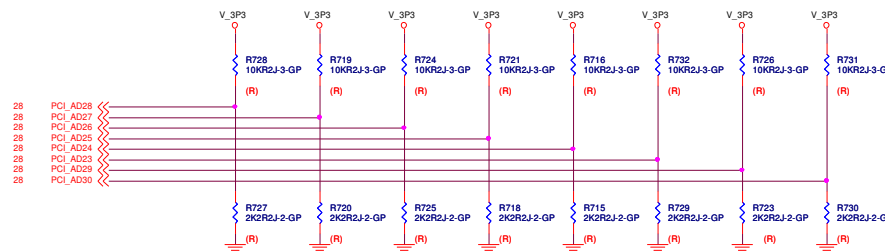
REQUIRED STRAPS

	AZ_SDOUT	PCI_CLK1	PCI_CLK2	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	GPIO200	GPIO199
PULL HIGH	LOW POWER MODE	ALLOW PCIE GEN2 DEFAULT	WATCHDOG TIMER ON NB_PWRGD ENABLED	USE DEBUG STRAPS	NON-FUSION CPU CLOCK MODE DEFAULT	EC ENABLED	CLKGEN ENABLED DEFAULT	ROM TYPE: H, H = Reserved H, L = SPI ROM DEFAULT	
PULL LOW	PERFORMANCE MODE DEFAULT	FORCE PCIE GEN1	WATCHDOG TIMER ON NB_PWRGD DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT	FUSION CPU CLOCK MODE	EC DISABLED DEFAULT	CLKGEN DISABLED	L, H = LPC ROM L, L = FWH ROM	

OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

DEBUG STRAPS

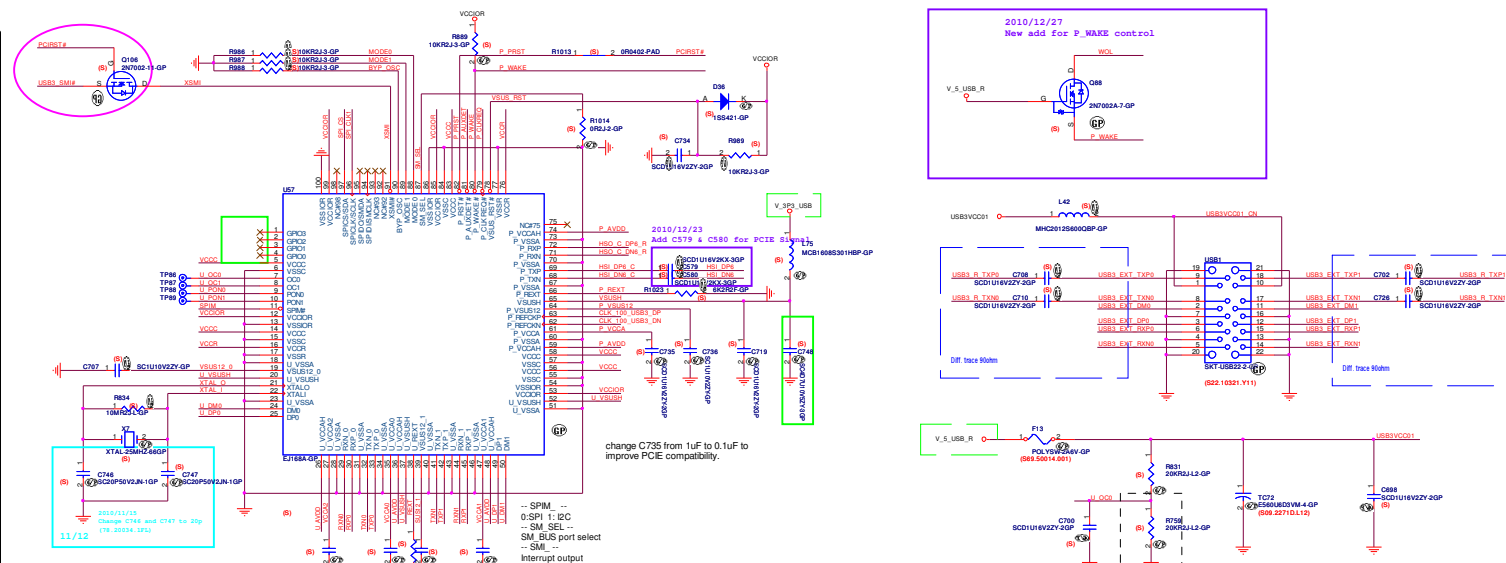
SB950 HAS 15K INTERNAL PU FOR PCI_AD[30:23]



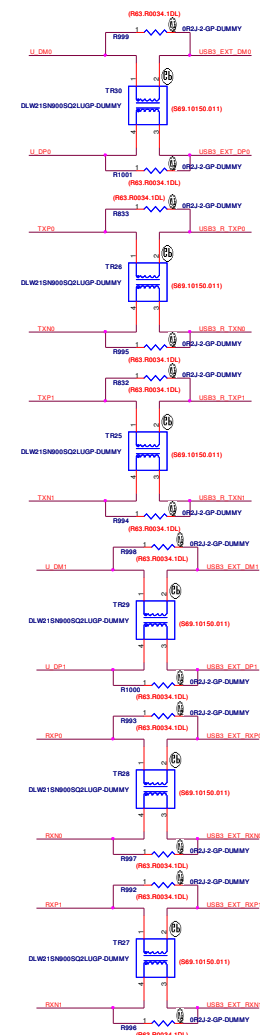
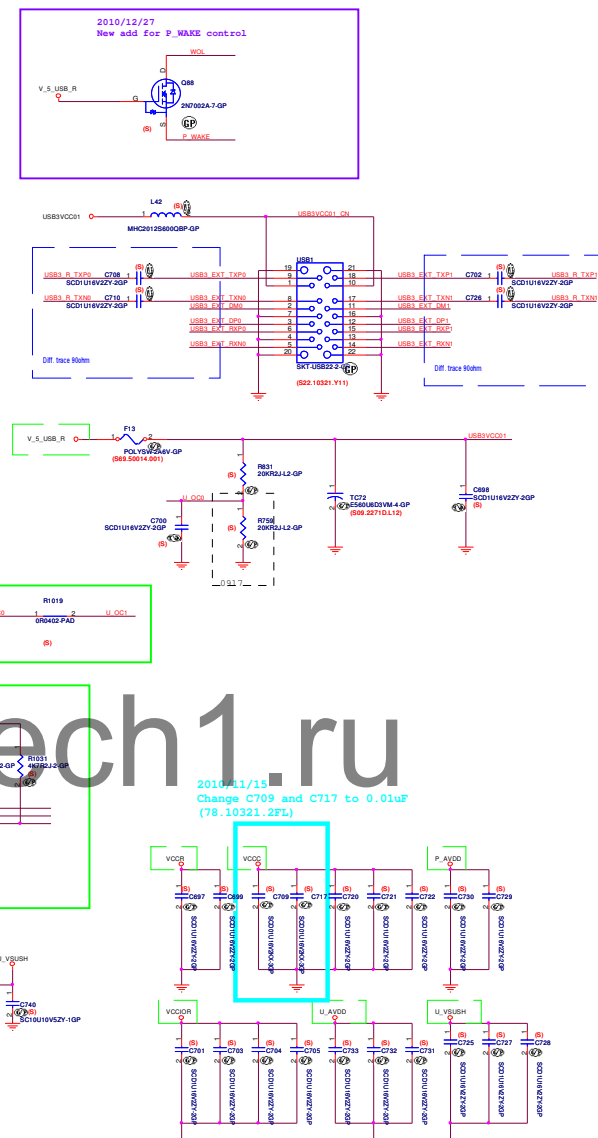
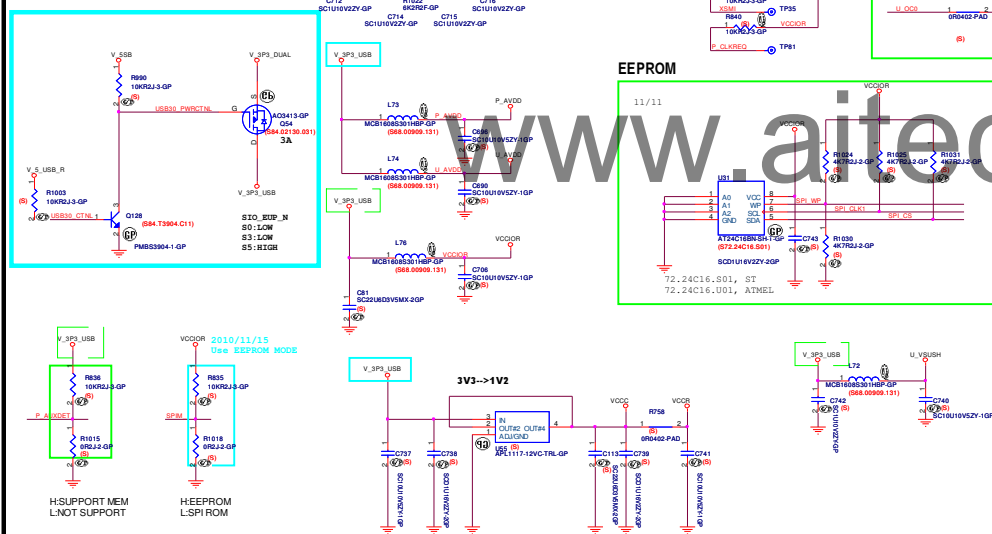
	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE PCI PLL DEFAULT	DISABLE ILA AUTORUN DEFAULT	USE FC PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
PULL LOW	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT

<Variant Name>

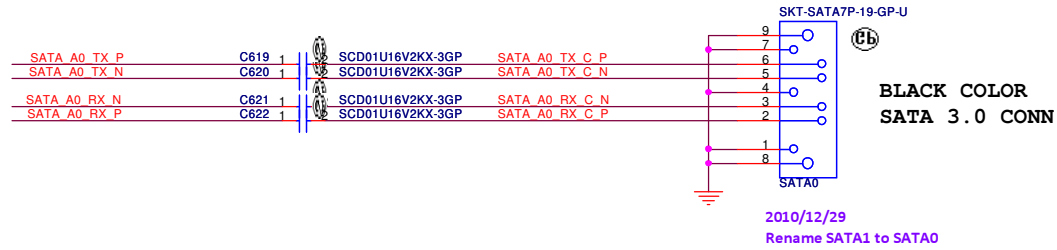
wistron		Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
File STRAPS			
Size	Document Number	Rev	
Custom	NADIA	SA	
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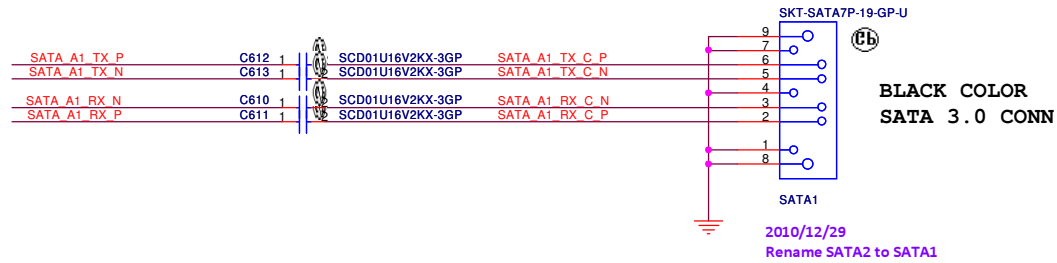
USB3.0 POWER CONTROL



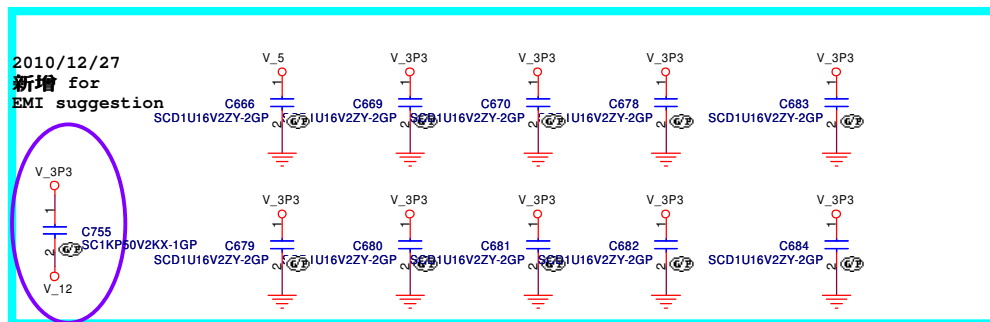
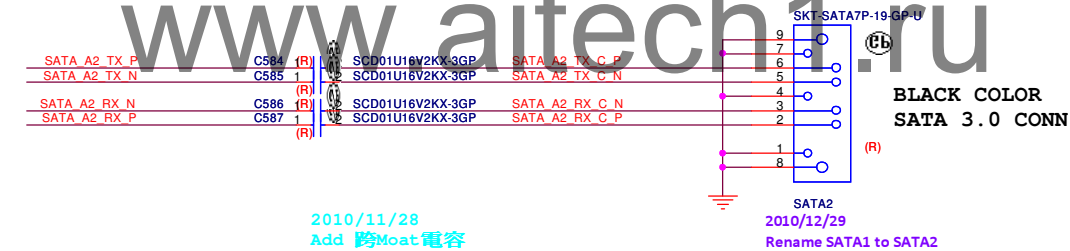
30 SATA_TXP0 >> SATA_A0_TX_P
30 SATA_TXN0 >> SATA_A0_TX_N
30 SATA_RXN0 >> SATA_A0_RX_N
30 SATA_RXP0 >> SATA_A0_RX_P




30 SATA_TXP1 >> SATA_A1_TX_P
30 SATA_TXN1 >> SATA_A1_TX_N
30 SATA_RXN1 >> SATA_A1_RX_N
30 SATA_RXP1 >> SATA_A1_RX_P



30 SATA_TXP2 >> SATA_A2_TX_P
30 SATA_TXN2 >> SATA_A2_TX_N
30 SATA_RXN2 >> SATA_A2_RX_N
30 SATA_RXP2 >> SATA_A2_RX_P



<Variant Name>

		Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
Title SATA CONN			
Size B	Document Number NADIA		Rev SA
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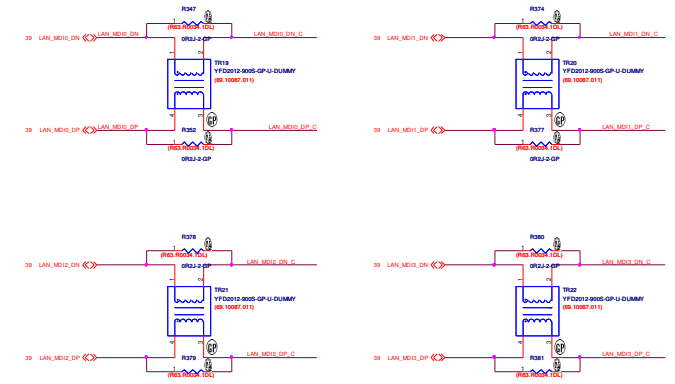
39 LAN_LINK_1000 >>>
39 LAN_LINK_1000 >>>
39 42 LAN_LED_Active >>>

39 LAN_MDIO_IN <<< LAN_MDIO_IN
39 LAN_MDIO_IN <<< LAN_MDIO_INP
39 LAN_MDIO_IN <<< LAN_MDIO_INP
39 LAN_MDIO_IN <<< LAN_MDIO_INP
39 LAN_MDIO_IN <<< LAN_MDIO_INP
39 LAN_MDIO_IN <<< LAN_MDIO_INP
39 LAN_MDIO_IN <<< LAN_MDIO_INP
39 LAN_MDIO_IN <<< LAN_MDIO_INP
39 LAN_MDIO_IN <<< LAN_MDIO_INP
39 LAN_MDIO_IN <<< LAN_MDIO_INP

29 USB_3_P <<<
29 USB_3_N <<<
29 USB_2_P <<<
29 USB_2_N <<<

29 USB_OC_2_3 <<<

```



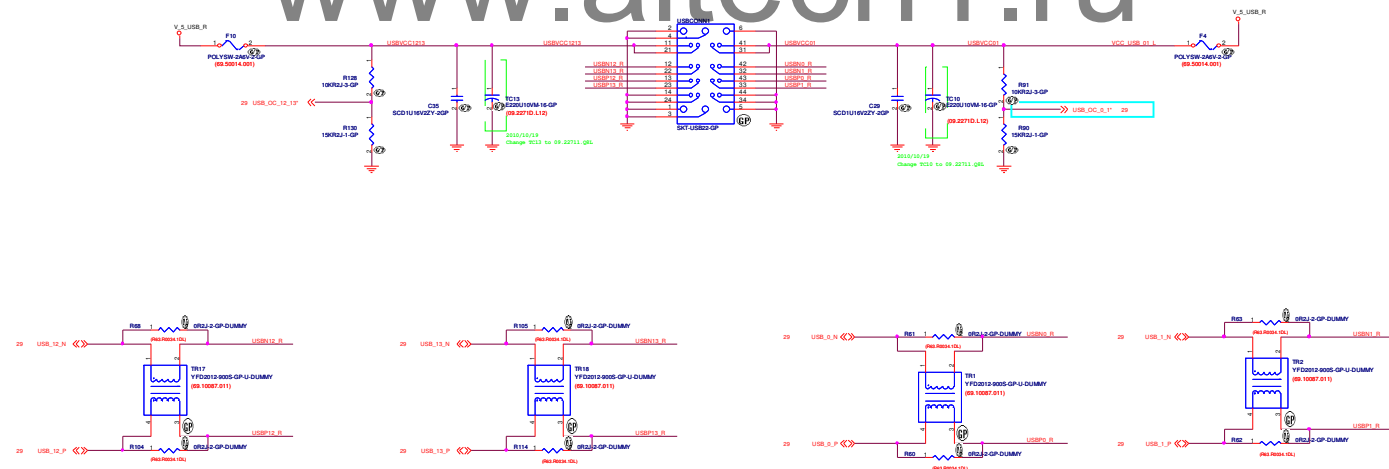
```

20      USB_0_N <= 0;
20      USB_0_P <= 0;
20      USB_1_N <= 0;
20      USB_1_P <= 0;

2020/10/5
New add
20      USB_12_M <= 0;
20      USB_12_P <= 0;
20      USB_13_M <= 0;
20      USB_13_P <= 0;

2020/10/5
add USB_OC_12_13*
20      USB_OC_12_13 <= 0;
20      USB_OC_0_1 <= 0;

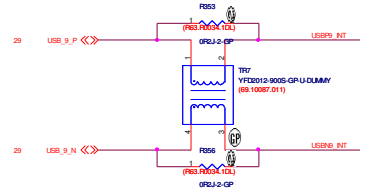
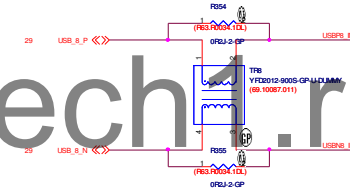
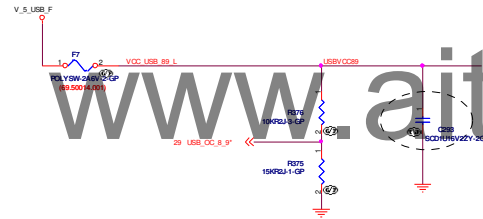
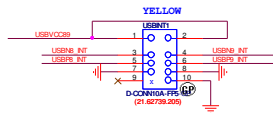
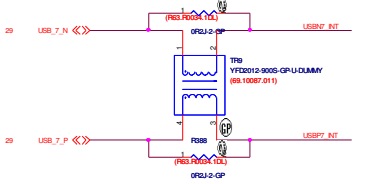
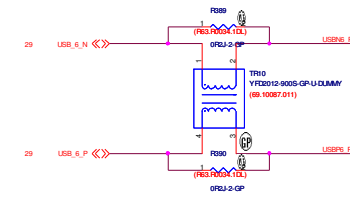
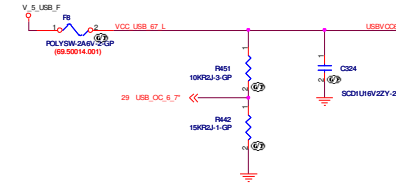
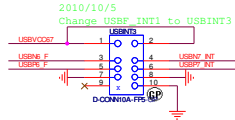
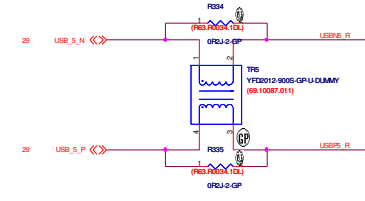
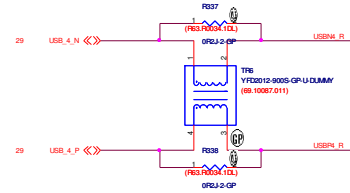
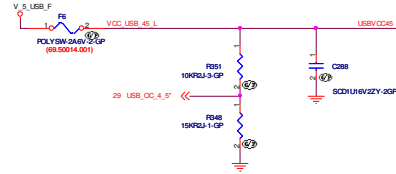
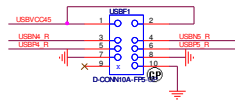
```



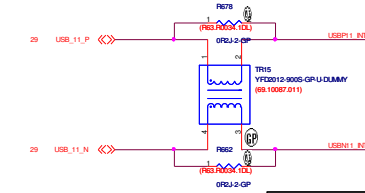
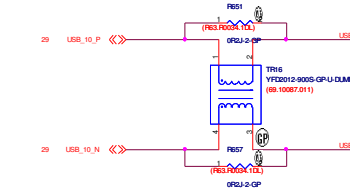
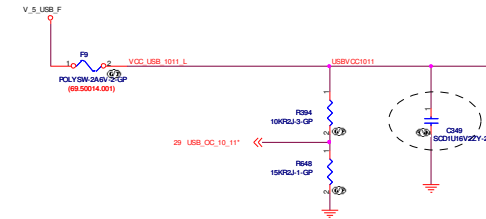
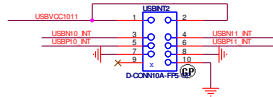
FRONT USB

29 USB_A_N <>
29 USB_A_P <>
29 USB_S_N <>
29 USB_S_P <>
29 USB_6_N <>
29 USB_6_P <>
29 USB_7_N <>
29 USB_7_P <>
29 USB_8_N <>
29 USB_8_P <>
29 USB_9_N <>
29 USB_9_P <>
29 USB_10_N <>
29 USB_10_P <>
29 USB_11_N <>
29 USB_11_P <>

29 USB_OC_4_5 <>
29 USB_OC_6_7 <>
2010/09/30
add USB_OC_10_11 <>
29 USB_OC_8_9 <>

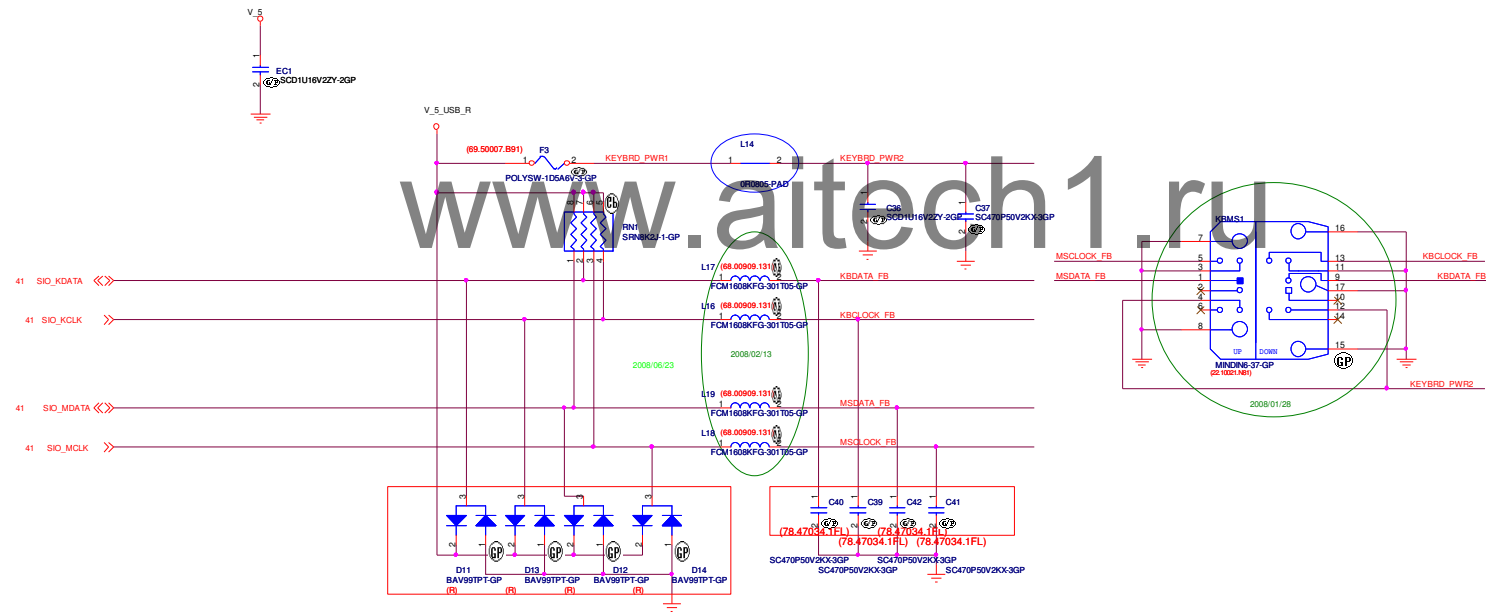


2010/09/30
New add USBIN2 related schematic



41 SIO_KDATA <<>>
41 SIO_KCLK >>
41 SIO_MDATA <<>>
41 SIO_MCLK >>

Delete COM Port



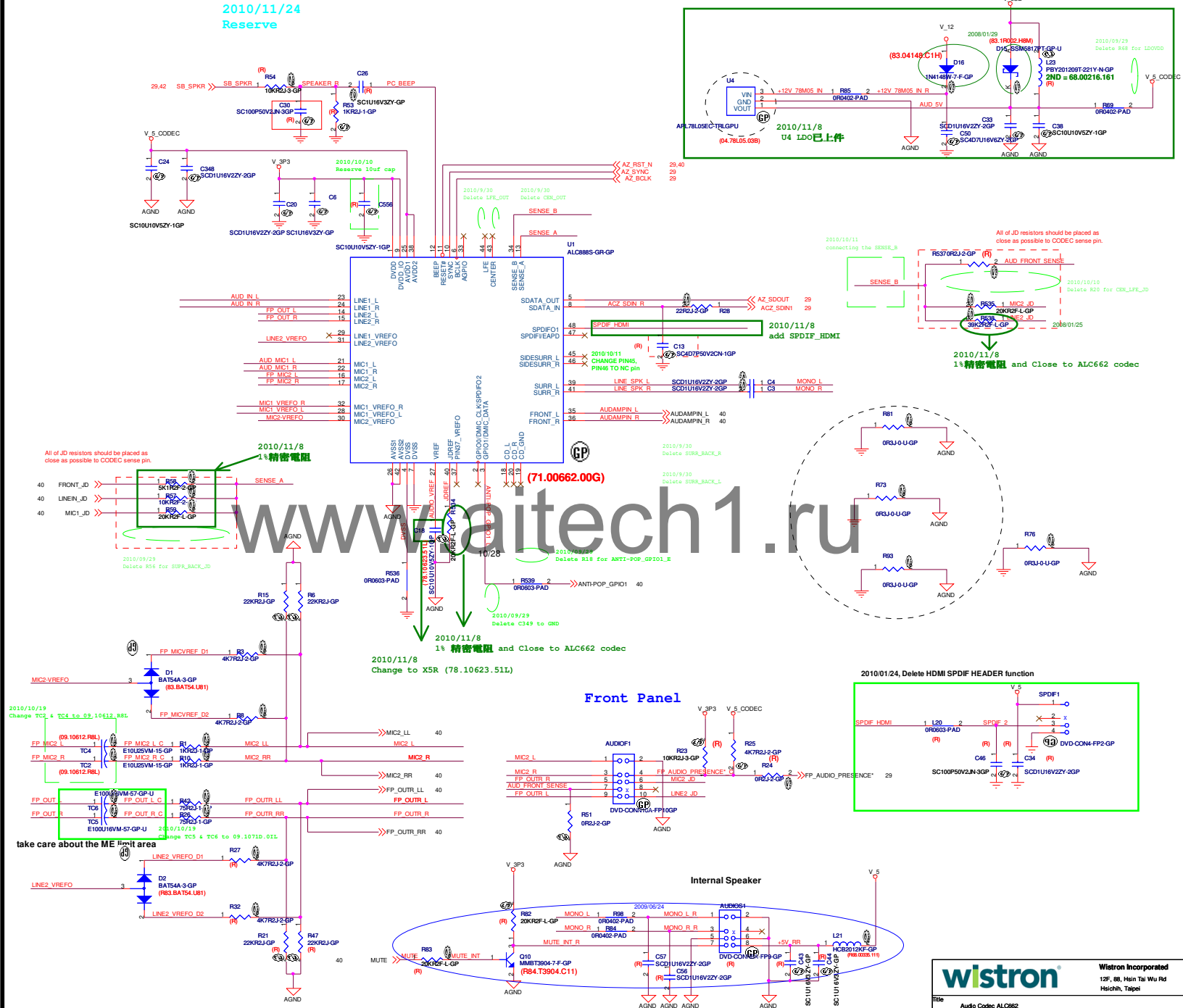
2010/11/24
Reserve

HDA

29 AZ_SDOUT
29 ACZ_SDIN
29 AZ_RST_N
29 AZ_SYNC
29 AZ_BCLK

AUDIO

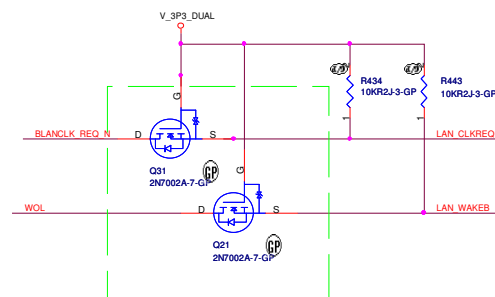
29.42 SB_SPKR
2010/10/5
Delete SPDIF_OUT1
40 AUD_IN_L
40 AUD_IN_R
40 AUD_MIC1_L
40 AUD_MIC1_R
40 MIC1_VREF0_R
40 MIC1_VREF0_L
40 FRONT_ID
40 LINEIN_ID
40 MIC1_ID
40 MIC2_LL
40 MIC2_RR
40 FP_OUTL_LL
40 FP_OUTL_RR
40 ANTI_POP_GPIO1
2010/10/5
Delete LFE_OUT
2010/10/5
Delete CEN_OUT
29 FP_AUDIO_PRESENCE



```

35 LAN_LINK_1000 <<
35 LAN_LINK_100 <<
35,42 LAN_LED_ACTIVE <<
16 HSO_BLAN_DP >>
16 HSO_BLAN_DN >>
16 HSI_BLAN_DP5 >>
16 HSI_BLAN_DN5 >>
28,33 PCIRST# >>
28 OK_BLAN_DP >>
28 OK_BLAN_DN >>
25,26,29,33 WOL <<
35 LAN_MID_DP >>
35 LAN_MID_DN >>
35 LAN_MDI_DP >>
35 LAN_MDI_DN >>
35 LAN_MID2_DP >>
35 LAN_MID2_DN >>
35 LAN_MID3_DP >>
35 LAN_MID3_DN >>
29 BLANCLK_REQ_N <<
41 LAN_RST# >>

```



2010/10/9
CHANGE Q31 AND Q21 TO 84.2N702.E31

```

2010/10/5
Delete CER_LFE_JD

2010/10/5
Delete LFE_OUT

2010/10/5
Delete CER_OUT

2010/10/5
Delete SUBR_BACK_JD

2010/10/5
Delete SUBR_BACK_L

2010/10/5
Delete SUBR_BACK_R

2010/10/5
Delete SFOIF_OUT1

```

2010/10/5
Delete 5 port Audio Jack

29.38 AZ_RST_N >>
ANTI-POP_GPO1 >>

38 LINEIN_ID <<LINEIN_ID
38 AUD_IN_L <<
38 AUD_IN_R <<

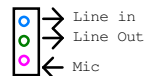
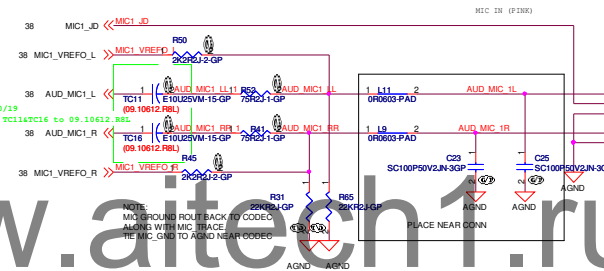
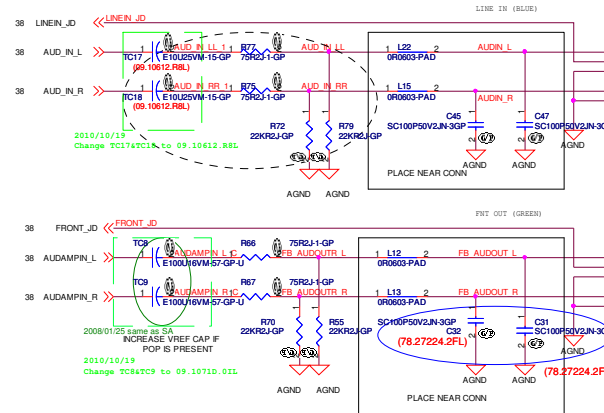
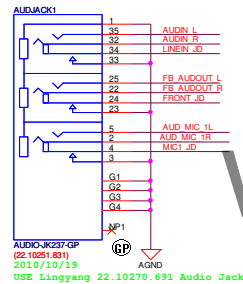
38 FRONT_ID <<FRONT_ID
38 AUDAMPIN_L <<
38 AUDAMPIN_R <<

38 MIC1_ID <<MIC1_ID

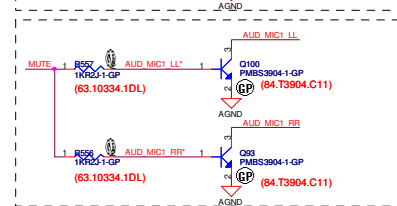
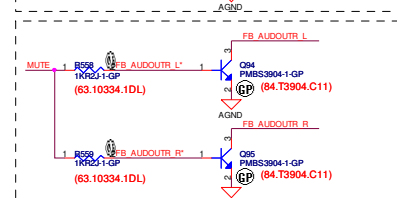
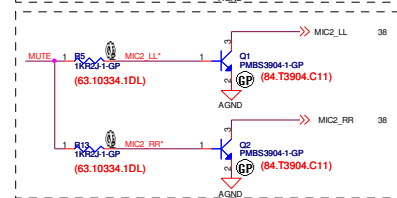
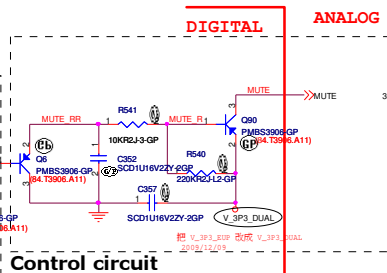
38 MIC1_VREF0_L <<MIC1_VREF0_L
38 AUD_MIC1_L <<
38 AUD_MIC1_R <<

38 MIC1_VREF0_R <<MIC1_VREF0_R

38 FP_OUT0R_LL <<
38 FP_OUT0R_RR <<
38 MIC2_LL <<
38 MIC2_RR <<

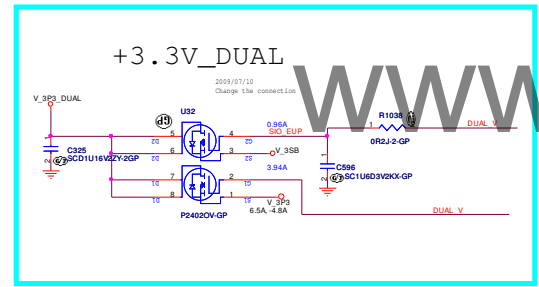
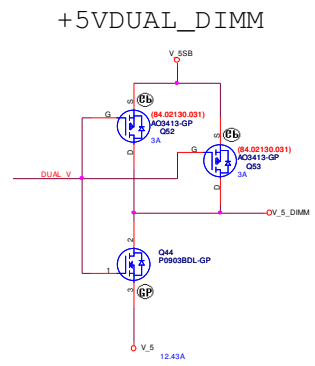
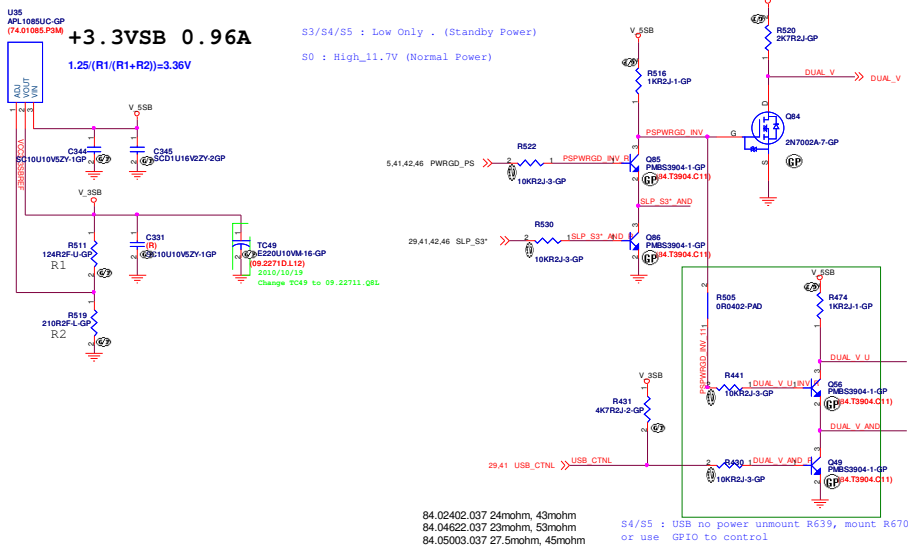


2010/10/14
Delete Mute schematic for
5 port audio jack

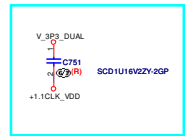
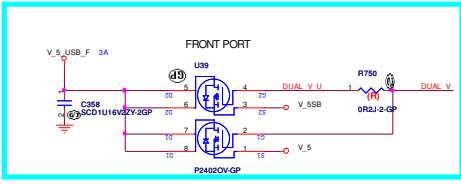
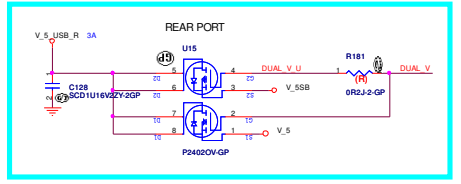
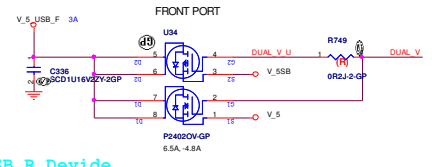
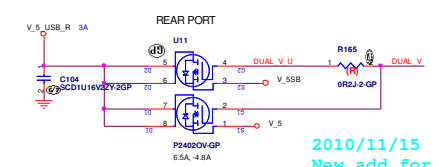
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Control circuit

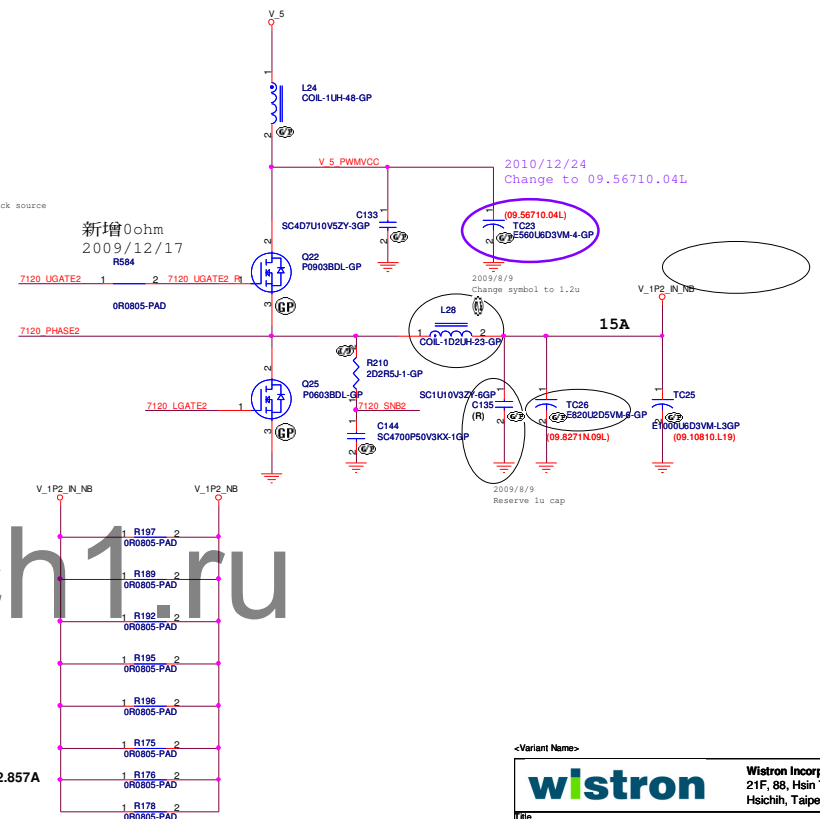
Dual Power Control



+5VDUAL_USB

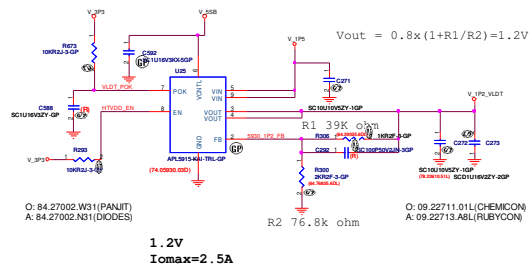
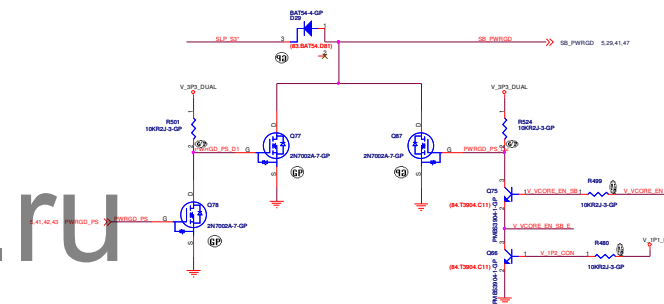
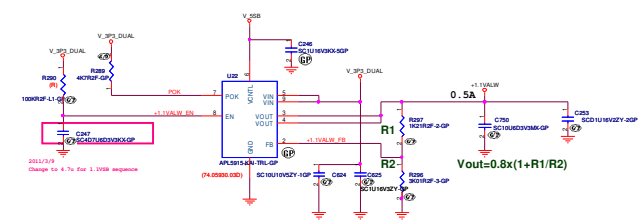


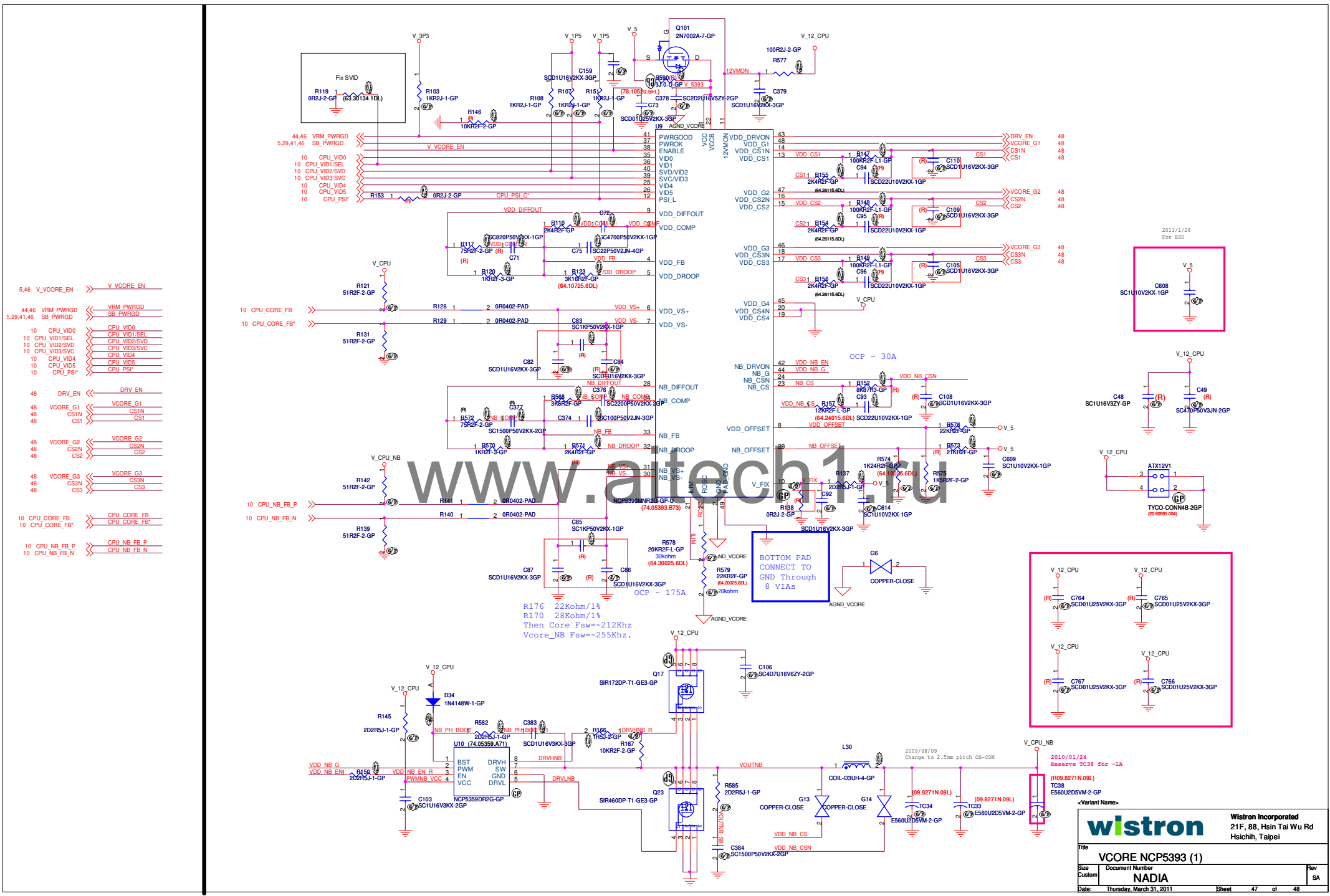
<Variant Name>		Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Heichin, Taipei	
wistron			
Title		DUAL POWER & 3.3/5V DUAL	
Size	Document Number	Rev	
Duston	NADIA	SA	
Date: Thursday, March 31, 2011		Sheet 43 of 48	



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Rocset=18K,Rdson=14mohm,Ipeak=22.857A





47 DRV_EN >> DRV_EN
47 VCORE_G1 >> VCORE_G1
47 CS1 >> CS1
47 CS1N >> CS1N
47 VCORE_G2 >> VCORE_G2
47 CS2 >> CS2
47 CS2N >> CS2N
47 VCORE_G3 >> VCORE_G3
47 CS3 >> CS3
47 CS3N >> CS3N

